

EECS 210 Section 2 – Lecture Summaries
Lecture 10, Monday, January 29, 2001

- Independent voltage sources in nodal analysis
- Mesh analysis (KVL)
 - Simplify without destroying problem
 - Choose method of solution
 - If using mesh equation method
 - Label current loops
 - Write KVL equation(s) for each loop with current-resistance products substituted for voltages
 - Independent current sources in mesh analysis
 - Solve
 - Check via
 - ✓ Does it seem right?
 - ✓ Nodal analysis
 - ✓ Tellegen

EECS 210 Section 2 – Lecture Summaries
Lecture 11, Wednesday, January 31, 2001

- Reviewed Mesh Current analysis
- Introduced Superposition

Can be used if all circuit elements are linear

Voltage or current at any point in a circuit is the sum of voltages or currents contributed by each independent source with all other sources killed