Saturation of Op Amp
- Leads to Clipping
- Distortion of input signal introduces higher order components in Fourier series representation of output

Energy Storage Devices
- Capacitor, \( q = C \cdot v; \ i = C \frac{dv}{dt} \) (passive sign convention)
  - \( i \) increases with \( C \) and with frequency
  - Voltage must be continuous
  - Capacitors in parallel add
    - Capacitors in series look like \( C_{eq} = \frac{1}{\frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3} + \ldots} \)