



If the verilog file for the adder were to make a schematic for its synthesized code it would generate a schematic like this.

Things to note:

co0,co1,co2 are all wires that connect the output of one adder to the input of another.

All connections across the top line are inputs to the fourBitAdder
Likewise, all connections across the bottom are outputs

Everything inside the box has been created in verilog and connected together in verilog