## **Quiz 4 – EECS 270, Spring '07**

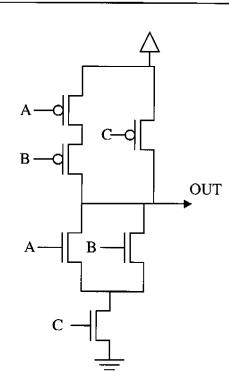
Name: KEY	unique name:KEV
Honor code: I have not given or received aid on this	quiz, nor have I observed anyone else doing so:
Sign here:	

This quiz is graded out of 100 points and is worth about 4% of your class grade. You will have **25** minutes for this quiz. *Closed everything including calculators!* To receive partial credit, work must be shown.

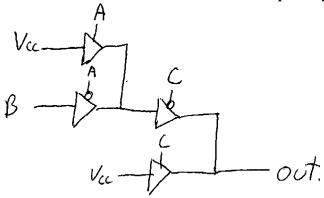
## 1. Transistor problem. [30]

A	В	C	OUT
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

Fill in the above truth table with either "1", "0", "Hi-Z" or "Smoke" (the last if OUT is connected to both Vcc and Ground).



2. Using only tri-state drivers and inverters, design a 3-input OR gate. You can freely use "GND" and "Vcc" as needed. For full points your design should use 8 or less devices. [35]



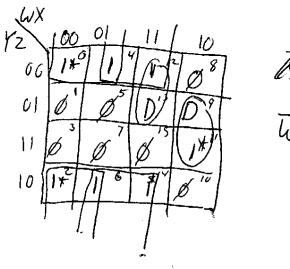
Note: A common error was to try to "push" Hiz

through a device A B C

If C.B, the output

is unknown!

3. Using a K-map, find the minimal sum-of-products of  $\Sigma_{(W,X,Y,Z)}$  (0,2,4,6,11,12,14)+d(9,13). Place a star (\*) next to each distinguished 1 in your K-map. [35]



TOX HX

WZ + XZ + WXZ