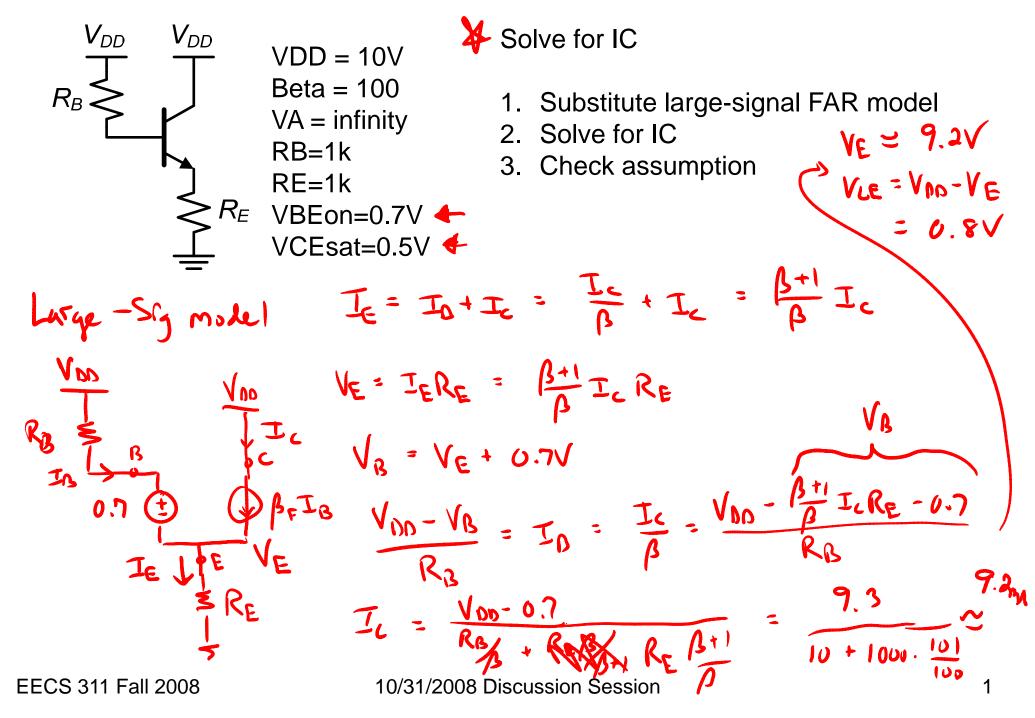
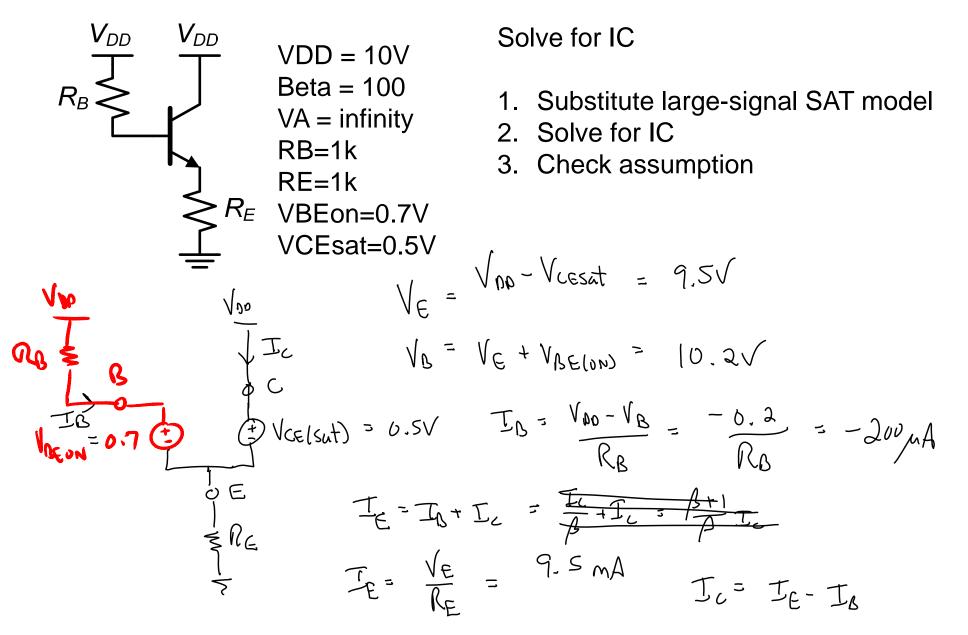
#### NPN Large Signal (Assume FAR)

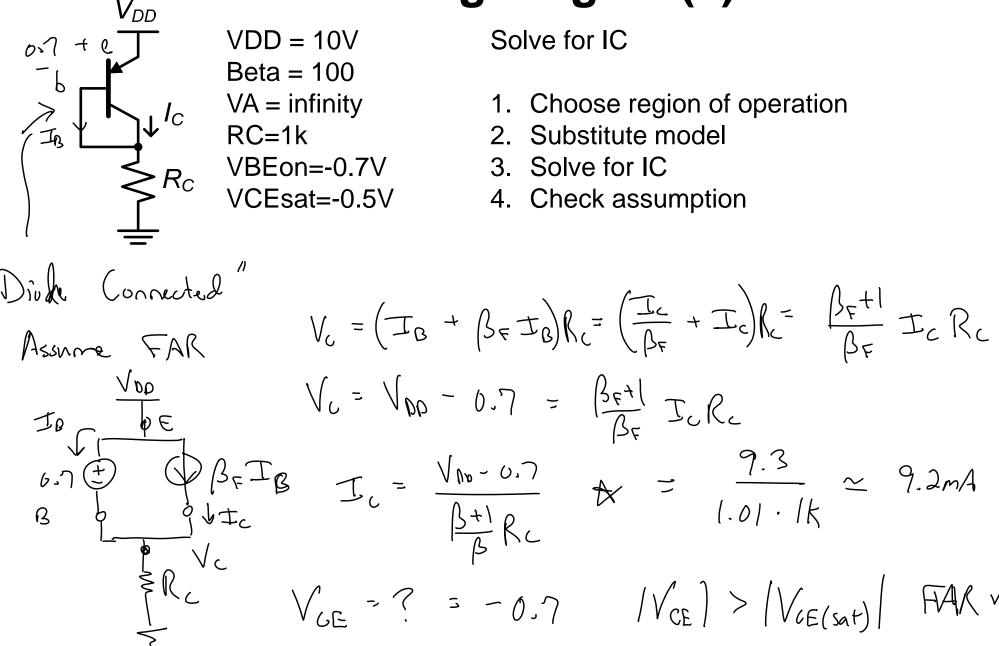


### NPN Large Signal (Assume SAT)



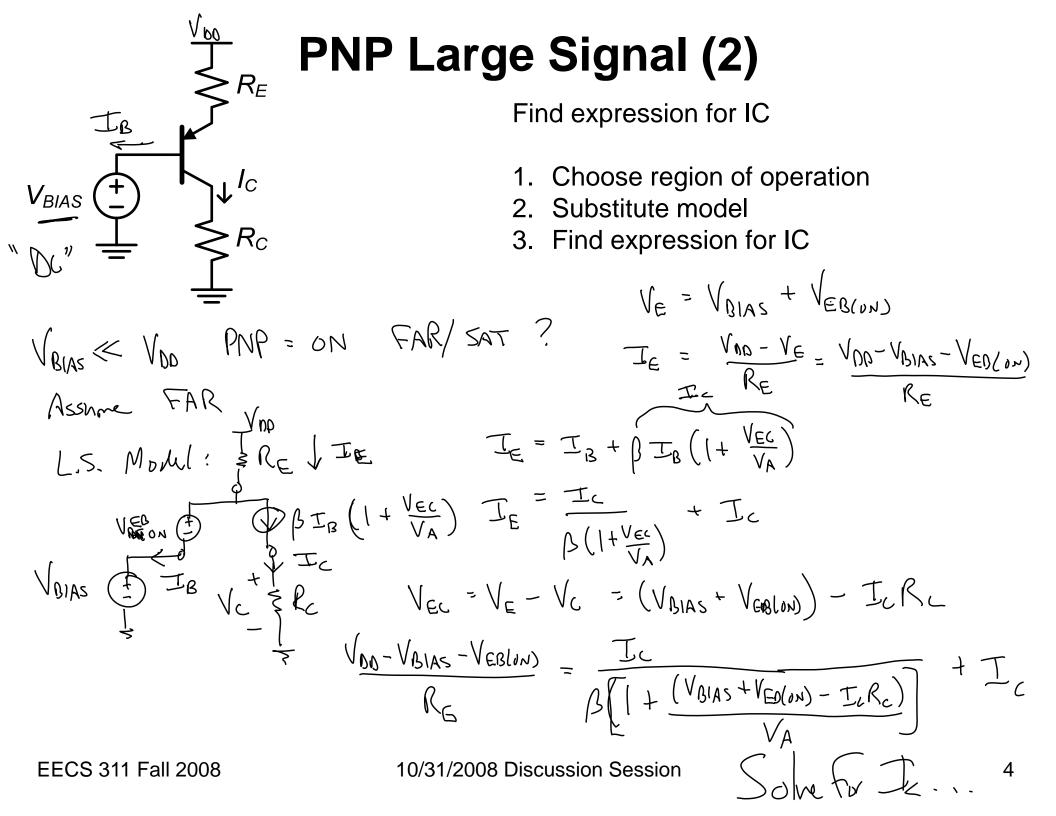
10/31/2008 Discussion Session

## **PNP Large Signal (1)**

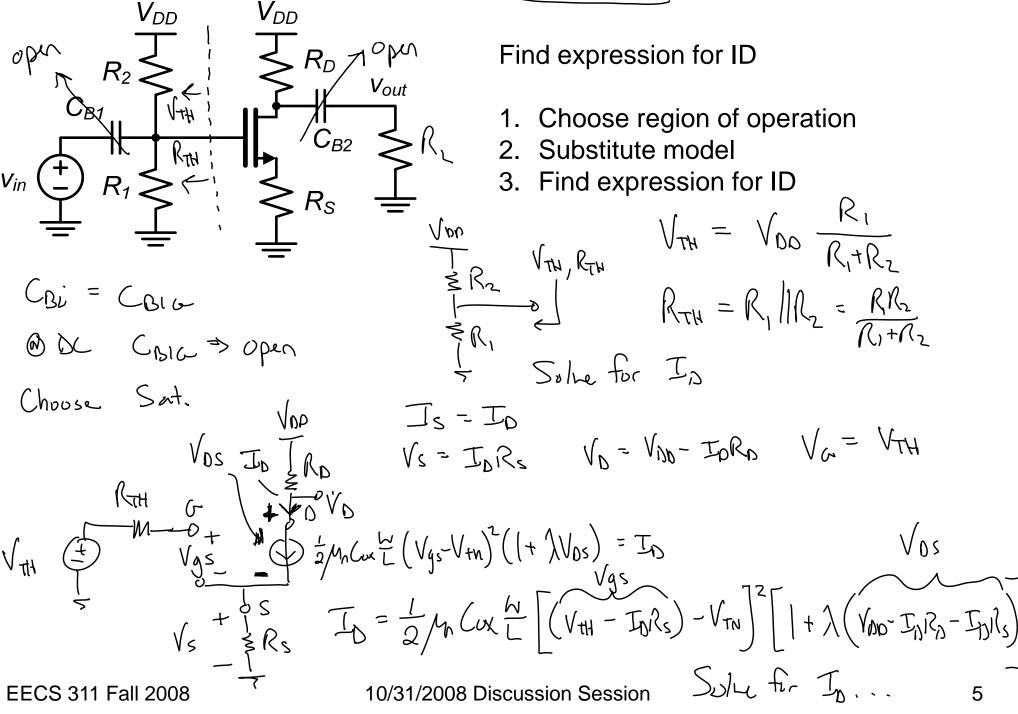


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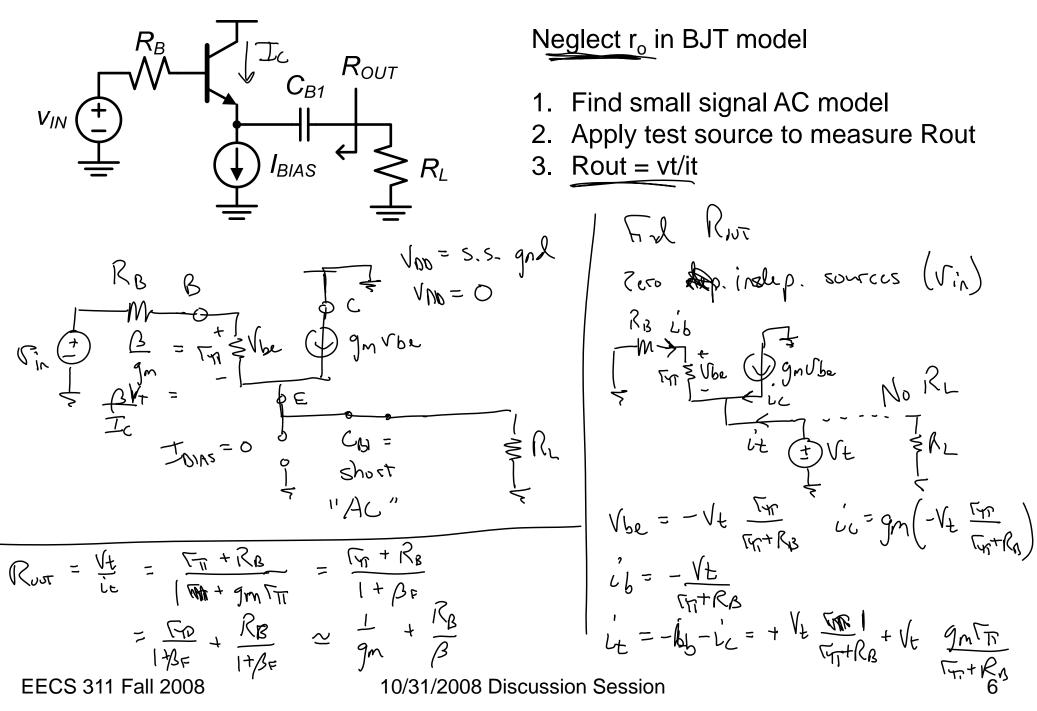
10/31/2008 Discussion Session



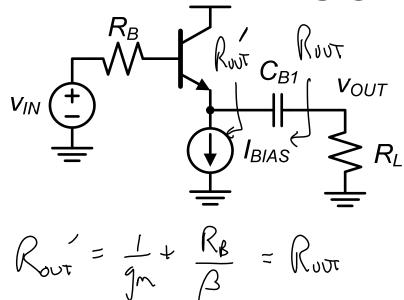
### NMOS DC Model



### **NPN Small Signal Rout**

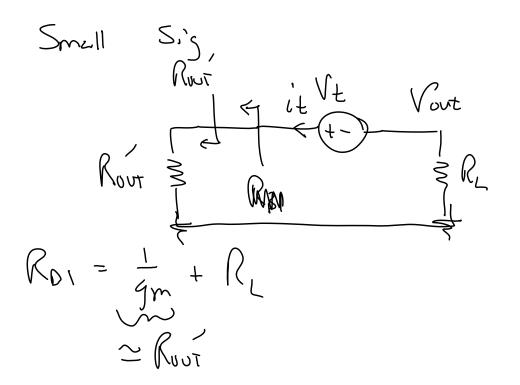


# OCTC to Find $f_L$



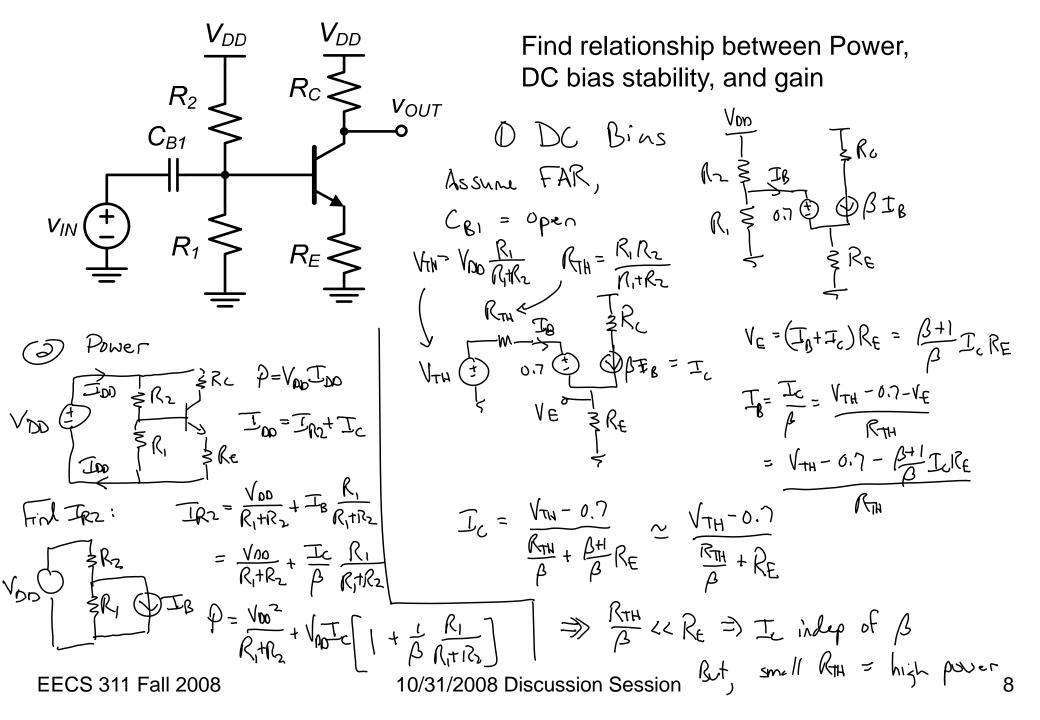
Find RBI For CBI > Tepluce CBI W/ Vt Neglect r<sub>o</sub> in BJT model

- 1. Find small signal low-freq model
- 2. Apply OCTC



BLTC

## NPN Design Example (1)



### NPN Design Example (2)

