University of Michigan EECS 311: Electronic Circuits Fall 2008

Quiz 1

10/6/2008

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NAME:	 0112	

Honor Code:

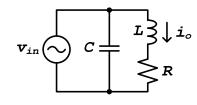
I have neither given nor received unauthorized aid on this examination, nor have I concealed any violations of the Honor Code.

Signature	

Problem	Points	Score	Initials
1	30		
2	34		
3	24		
4	12		
	Total		

Problem 1 (30 Points): Potpourri – this problem has five unrelated parts.

a) Find the transfer function $H(s) = i_{out}/v_{in}$ of the following circuit.



C does not impact io
$$\dot{l}_0 = \frac{V_{in}}{R + sL} \qquad \frac{\dot{l}_0}{V_{in}} = \frac{l}{R} \cdot \frac{l}{1 + s \, \forall R}$$

b) Sketch the magnitude of the following transfer function on the following log-log plot. Label the DC magnitude, the high-frequency magnitude, and the frequencies of any poles and zeros.

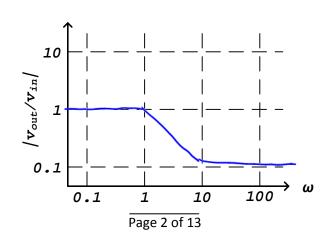
$$\frac{v_{out}}{v_{in}} = \frac{0.1s + 1}{s + 1}$$

$$DC S = 0 \quad |A_{i}| = 1$$

$$high f S = \infty \quad |A_{i}| = 0.1$$

$$pole \quad 0 \quad |A_{i}| = 1$$

$$V_{out} = \frac{0.1s + 1}{s + 1}$$



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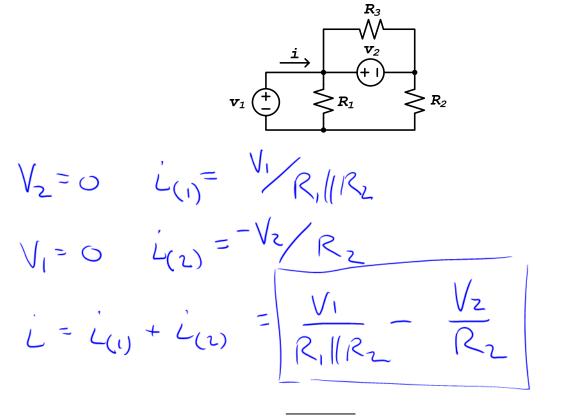
c) Sketch the step response of the following transfer function. Label the initial value ($t = 0^+$), final value ($t = \infty$), and time constant on the graph.

Initial value theorem
$$S \to \infty$$
 Vont $(t=0) = 0.1 \text{ V}$

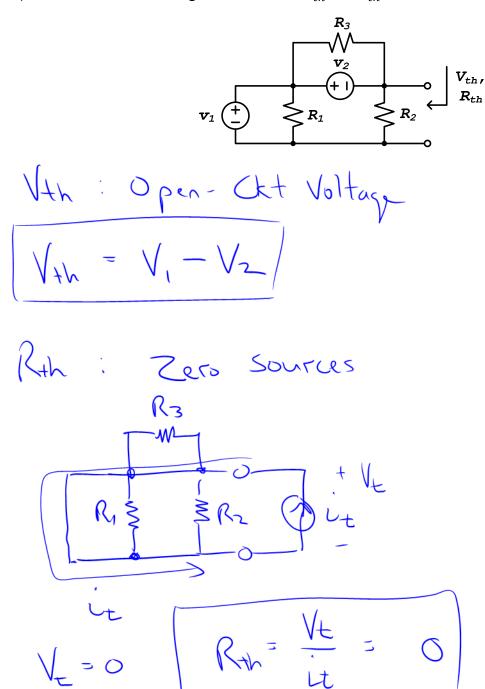
Final value theorem $S \to \infty$ Vont $(t=\infty) = 1 \text{ V}$

I pole at $Dp = 1 \Rightarrow T = \frac{1}{Dp} = 1 \text{ S}$
 v_{out}
 $v_{\text{out}} = \frac{1}{Dp} = 1 \text{ V}$
 $v_{\text{out}} = \frac{1}{Dp} = 1 \text{ V}$

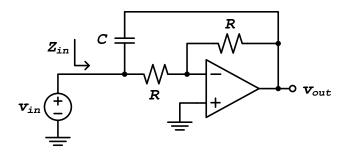
d) Solve for the current i in the following circuit using superposition.



e) Find the Thevenin voltage and resistance ${\it V}_{th}$ and ${\it R}_{th}$ as shown in the circuit below.



Problem 2 (34 Points): Use the following circuit for this problem. Assume the opamp is ideal unless otherwise stated in the problem.



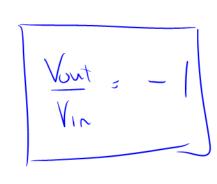
a) Derive an expression for the transfer function $H(s) = v_{out}/v_{in}$.

Vout Via is indep

ider o

Vin - 0

O-Von



b) Derive an expression for the input impedance $Z_{in}(s)$ as defined in the schematic.

$$V_{t} = \frac{V_{t}}{V_{t}}$$

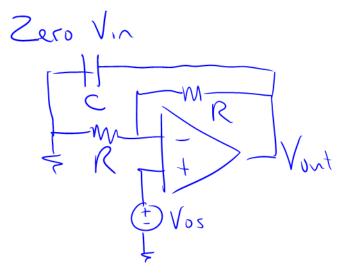
$$V_{t} = \frac{V_{t}}{V_{t}} + \frac{V_{t} - V_{out}}{V_{sc}}$$

$$V_{t} = -V_{in} \left(\frac{V_{t}}{V_{sc}} + \frac{V_{t}}{V_{sc}} \right)$$

$$V_{t} = \left(\frac{1}{R} + sac \right) V_{t}$$

$$Z_{in} = R \cdot \frac{1}{1 + sac}$$

c) Assume the opamp has a finite offset voltage V_{OS} . Derive an expression for the transfer function $K(s) = v_{out}/V_{OS}$.



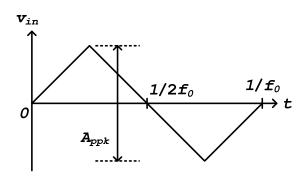
Non-inverting amp

- $\frac{\sqrt{\text{out}}}{\sqrt{\text{os}}} = \left(+ \frac{R}{R} \right) = \left[\frac{1}{2} \right]$
- d) Assume the input offset voltage is a DC voltage with amplitude V_{OS} . With the input set to zero ($v_{in}=0$), derive an expression for the magnitude of the output voltage $|v_{out}|$ at time $t=\infty$ resulting from the offset voltage.

At t= N

Vont = 2 Vos

e) Assume the input v_{in} is a triangle wave with peak-peak amplitude A_{ppk} and frequency f_0 . Derive an expression for the minimum allowable slew rate specification of the opamp for the circuit to properly amplify and track this input waveform.



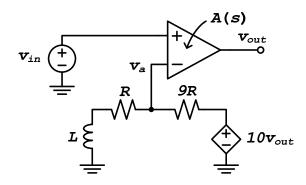
From a), Vont = - Vin

Slew Rate > max 3 Vout

$$\frac{\partial V_{\text{out}}}{\partial t} = \frac{\Delta V}{\Delta t} = \frac{Appk}{\left(\frac{1}{2F_0}\right)} = 2F_0$$

S.R. > 2 F. Apple

Problem 3 (24 Points): This problem uses graphical filter design techniques to find the transfer function of a filter. Use the following schematic for all parts of this problem.



a) Assume the opamp is ideal (infinite gain and bandwidth). Derive an expression for the transfer function $H(s) = v_{out}/v_{in}$.

b) Derive an expression for the feedback factor $\beta(s) = v_A/v_{out}$ as defined in the schematic.

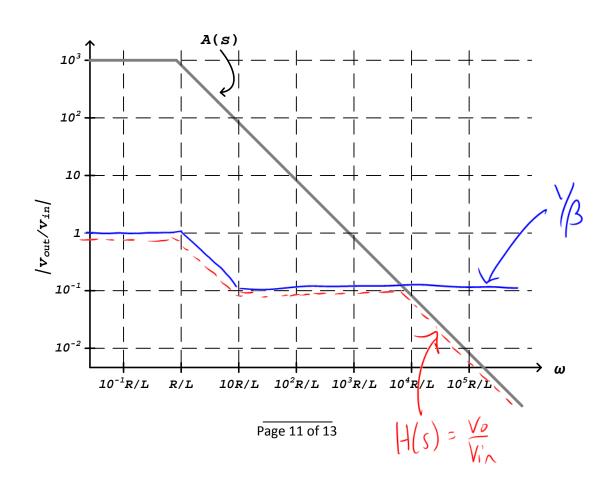
c) Using graphical filter design techniques, sketch the magnitude of the transfer function $H(s) = v_{out}/v_{in}$ on the graph below. Assume the opamp has finite gain and bandwidth as shown in the graph.

$$\beta = \frac{1+5 \frac{1}{1}}{1+5 \frac{1}{10R}}$$

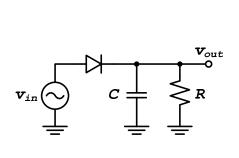
$$Plot \beta = \frac{1+5 \frac{1}{10R}}{1+5 \frac{1}{1R}}$$

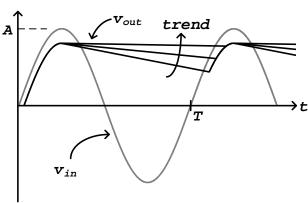
$$Pole \omega \omega p = \frac{R}{L}$$

$$Lower of \beta A(s) is \frac{Vort}{Vir}$$



Problem 4 (12 Points): This is a multiple choice problem. Use the following page for additional work if needed. For each part, circle only one answer. Refer to the following diode circuit and waveforms for each part. Assume the *constant voltage source model* for the diode, with an on-voltage of V_{ON} .





a) Keeping everything else constant, the trend indicated on the output waveform will be observed as the value of \mathcal{C} is:

Increased | Doesn't cause trend

b) Keeping everything else constant, the trend indicated on the output waveform will be observed as the value of R is:

Increased | Decreased | Doesn't cause trend

c) Keeping everything else constant, the trend indicated on the output waveform will be observed as the period of the input sine wave T is:

Increased | Decreased | Doesn't cause trend

d) Keeping everything else constant, the trend indicated on the output waveform will be observed as the amplitude of the input sine wave A is:

Increased | Decreased | Doesn't cause trend

Initials:	

(Space for additional work)