

EECS 370

Discussion 5

Week of 2/13 – 2/19

- I) Example problems from previous midterms
 - a. Requests for topics to review
 - b. New ISA design
 - i. Fall 2008, Question 4
 - c. RISC vs. CISC
 - i. Winter 2008, Question 1
 - d. CLA Adder
 - i. Winter 2008, Question 4
 - e. Caller/callee saved registers
 - i. Winter 2008, Question 7
 - ii. Fall 2005, Question 2
 - f. Load-store architectures
 - i. Fall 2007, Question 1a
 - g. Memory addressing
 - i. Fall 2007, Question 3
 - h. Memory layout
 - i. Fall 2007, Question 4
 - i. Combinational logic
 - i. Winter 2006, Question 5
 - j. Assembly with jalr
 - i. Fall 2005, Question 7
 - k. Byte alignment
 - i. Fall 2005, Question 10
- II) List of other topics to know (not exhaustive)
 - a. IEEE floating-point format
 - b. Finite state machines
 - c. Big/little endian
 - d. Symbol and relocation tables
 - e. Linking object files
 - f. Recursion in assembly
 - g. Function calls in assembly
 - h. LC-2K9 and MIPS ISA's
 - i. Logic gates
 - j. Single cycle datapath