

	Day		Lecture topic	Lab
1	Tuesday	1/6	Class intro, PPC assembly and arch.	Lab 1: intro to the logic analyzer
2	Thursday	1/8	PPC assembly and arch	
3	Tuesday	1/13	Bus basics and simple I/O	Lab2: Intro to single step and the assembler
4	Thursday	1/15	Bus basics and simple I/O	
5	Tuesday	1/20	Digital logic design	Lab3: Basic bus interfacing
6	Thursday	1/22	Digital logic design	
7	Tuesday	1/27	Memory	Lab 3 continued
8	Thursday	1/29	More Memory	
9	Tuesday	2/3	Stacks, Procedures and the ABI	Lab 4: Memory Interfacing
10	Thursday	2/5	Interrupts	
11	Tuesday	2/10	Interrupts	Lab 5 Serial I/O
12	Thursday	2/12	Yet more interrupts	
13	Tuesday	2/17	DMA	Lab 6 Interrupts
14	Thursday	2/19	<u>Midterm: 6pm-8pm</u>	
	Tuesday	2/24	Spring Break	
	Thursday	2/26		
15	Tuesday	3/2	Timers	Lab 6 continued
16	Thursday	3/4	Analog to Digital	
17	Tuesday	3/9	Analog to Digital	Lab 7: Timers
18	Thursday	3/11	TBD	
19	Tuesday	3/16	More bus stuff (PCI, P6)	Lab 8: Andalog to Digital Conversion
20	Thursday	3/18	Yet more bus stuff	
21	Tuesday	3/23	TBD	Project
22	Thursday	3/25	Wrapup/review	
23	Tuesday	3/30	<u>Final exam 6pm-8pm</u>	Project
24	Thursday	4/1	No lecture*	
25	Tuesday	4/6	No lecture*	Project
26	Thursday	4/8	No lecture*	
27	Tuesday	4/13	No lecture*	Project
28	Thursday	4/15	No lecture*	
29	Tuesday	4/20	No lecture*	Project Demo

Note: Schedule is tentative and very likely to change

* On days listed as no lecture we may have guest lecturers. Otherwise you will have this time to work on your project.