

Week	Week of Monday	Topic	PRL DUE	INL DUE	POL DUE	Notes	Value %
1	3-Jan	classes begin, no lab				Read Lab 1	
2	10-Jan	L1, Introduction to the Logic Analyzer and Xilinx		1		No PRL1 or POL1	1
3	17-Jan	L2, Introduction to the SingleStep and Assembler	2	2		No MONDAY LABS <b>NOTE 1</b> No POL2	2
4	24-Jan	L3, Simple Bus Interfacing	3				7
5	31-Jan	L3, Simple Bus Interfacing continued		3			
6	7-Feb	L4, Bus Interfacing for Byte Addressable Memory	4	4	3		5
7	14-Feb	L5, ABI, MetaWare C Compiler and Serial Communications	5	5	4		5
8	21-Feb	L6, Introduction to Interrupts	6		5		7
9	28-Feb	spring break					
10	7-Mar	L6, Introduction to Interrupts continued		6			
11	14-Mar	L7, Interrupts and Timers	7	7	6	Project Overview	5
12	21-Mar	L8, Analog to Digital Conversion	8	8	7	Project Advising, all week	7
13	28-Mar	Projects			8	Proposal Due	
14	4-Apr	Projects					
15	11-Apr	Projects					
16	18-Apr	Projects				Project Presentations	

**Pre-Lab (PRL)** Due in assigned section.

**In-Lab (INL)** All sections due Friday at the close of the open lab unless otherwise indicated.

**Post-Lab (POL)** Due in assigned section.

**NOTE1** MLK day, no Monday labs. Monday lab section prelab 2 due Tuesday 1/18 during any lab section. Monday lab sections need to make up the lab during the open labs that week. Monday open labs have until the following Monday 1/24 to complete in lab 2.