Finishing up
Design expo

- Design Expo is on Tuesday the 22\textsuperscript{nd} from 11am-1:30pm in the EECS atrium.
  - Setup starts at 9:30am
  - We’ll be moving much of the lab down.
  - It’s important that the lab get back into reasonable shape by 3pm.
    - Everything put back, nothing left laying out.
- Each group will need to
  - Have at least one person there at any given time.
    - Two is a lot better.
  - Have as many people as possible help with setup and teardown.
    - Each group does its own setup and teardown.
Design Expo—Working Demo

• You will need to have a working demo.
  – Think about how the demo will work.
    • Can others interact with it?
    • Do you need a lot of space or some other resource?
  – Think about resources needed.
    • We strongly prefer you not need a computer from lab.
      – Ideally use flash.
    • If you need a network connection, I need to know today.
    • We’ll be unlocking the power supplies and stuff.
      – But think about the need for scopes etc.
Design Expo—Grading

• Matt and I will be doing the project evaluation during the expo.
  – You need to have a demo sheet (see project description) printed.
    • Note: this includes work distribution.
  – We’ll ask for a demo and ask a lot of questions.

• The plan is that grades will be handed out when you leave the final exam
Project poster

• Posters are to be 32x40
  – Landscape or portrait both fine.

• Matt will e-mail out directions about how to print.
  – We’ll pay for one per group. Any reprints/error fixes are up to you to pay for.
Purpose of the poster

• To give people something to read while you are talking to someone else.
  – Good pictures
  – Solid and clear details

• To help draw people in.
  – If you project doesn’t move, you need something!
  – Pictures!

• To give you something to point to as you talk
  – For parts of your talk you can point to the project
    • But basic ideas, including function and purpose need something more.
  – Pictures
    • And generally not of your project (it will be there too).
      – Sometimes you want pictures of it doing something though
Target audience

• You’ll have folks ranging from embedded systems experts to freshmen asking questions and reading the poster.
  – The trick is to keep a narrative flowing with details available.
    • Generally “walls of text” can be okay for the detailed stuff.
Image issues

• Be really sure you have high-resolution images.
  – Things generally look better on the screen than they do printed.
  • As an overkill rule-of-thumb, make it 2x as large on the screen as it will be printed. If that looks good, you should be golden.

• At least a handful of images should be clear from 7 feet away.
  – Put it on the screen in normal size and stand 7 feet away.
  – Some, more detailed, images can be smaller. Use that carefully.
Text issues

• Title and major topics should be easily readable at 7 feet.
  – Detailed text can be smaller.
Narrative clarity

• It should be clear what images and text go together.
  – If there is an ordering things should be read in, that should be clear.
Required things

- Clear title
- List of students
- Course identified (EECS 373)
- Department and/or College Logo
Things you might want

• References/further reading
• Costs?
Project video

• You need to create a video of your project.
  – Posted on YouTube or similar
  – It should:
    • Show the project working
    • List who worked on it and that it is for 373 at the University of Michigan
    • Give a basic overview of how it works.
  – We aren’t looking for professional quality (though that would be wonderful).
    • Just want an overview.

• Due by Monday at 9pm
  – Need not take more than an hour to do.
Guest speakers

• We have two guest speakers on Thursday
  – Recall this is the one lecture where I’ll take attendance.
    • Counts as part of homework.
  – Speakers should be quite good.
    • One is a grad from EECS and 373 some 6 years ago. Works locally on embedded systems.
    • One is much more senior and has worked in a huge range of jobs including: Motorola cellular, dot-com game startup, HVAC manufacturer, and GE aerospace.
      – Currently in R&D modeling (all) systems for planes.
Final exam

• There are two old exams posted
  – As well as an old practice exam.

• Final exam will cover:
  – Material from the first exam (55%?)
    • Another design question likely.
  – ADCs and DACs (20%?)
  – PCB and power (5-10%)
  – RTOS (5-10%?)
  – Student talks (10%?)
Final exam

• Material on student talks will be about 1 point per talk
  – Multiple choice/fill in the blank most likely.
    • Suggest reviewing each for 10 minutes.
      – Identify what the 2-3 main points of each talk was.

• Higher expectations of material from first half than on midterm.
  – Had time to learn.
  – Be sure you can do MMIO and interrupts.
    • If you can’t, exam will go poorly.