

EECS 452 – DSP Design Laboratory

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Theory: amplitude quantization, FIR and IIR filters, oversampling, sigma/delta conversion.
Hardware: TI TMS320VC5515 USBstick and Altera DE2-70 FPGA Starter Board.
Software: C, Verilog, MATLAB, Code Composer Studio, Altera Quartus.

Teams:
Real-Time Video Stabilization
Motion Controlled DJ
3-D Sound Synthesis System
Storage Coordination System
Audio and Video Gaming Control

