EECS 452 Schedule (Winter 2014, EECS 1003)

WeekDayLectures, LabsItems due, reminder0Fri 1/10L1: Class introduction, C and DSP reviewMakeup class; 3-4:30pm1Lab 1: Introduction to the CS515 eZDSP StickPrelabs due b/f labMon 1/13L2: DSP review, filters, DDSHW1 due; PPI due2Lab 2: Basic DSP using the C5515 eZDSP StickHW1 due; PPI due2Lab 2: Basic DSP using the C5515 eZDSP StickHW2 dueMon 1/20No lecture (MLK day)HW2 dueWed 1/22L4: Gate-level arithmetic; project pitchHW2 dueFri 1/24L5: Logic, FPGA overview, the DE2-70 boardAdditional class; 3-4:30pm3Lab 3: Introduction to the DE2-70 FPGA boardGroup formation 6:30pm;Wed 1/29L7: DAC, oversampling and equalization, digital filter designHW3 due4Lab 4: DSP on the DE2-70 FPGA boardHW3 due4Lab 5: SIR filter design, roundoff and overflowWed 2/5Wed 2/5L9: Filter design continuedFri 2/7Project proposal presentations 10:30-12HW4 dueFri 2/1Project proposal presentations 10:30-12Wed 2/12Project proposal presentations 5-7pmRoom 10056Lab 6: Interrupts, FFT and graphicsMon 2/24L12: Midterm reviewWed 2/25No lecture (midterm study); Midterm 5-7pm in DOW 10137Optional Lab 7: SPL 12S or video processing7Optional Lab 7: SPL 12S or video processing7Optional Lab 7: SPL 12S or video processing7No classes; winter break9Mon	
Mon 1/13L2: DSP review, filters, DDSWed 1/15L3: Number representation; fixed point arithmeticHW1 due; PPI due2Lab 2: Basic DSP using the C5515 eZDSP StickMon 1/20No lecture (MLK day)Wed 1/22L4: Gate-level arithmetic; project pitchHW2 dueFri 1/24L5: Logic, FPGA overview, the DE2-70 boardAdditional class; 3-4:30pm3Lab 3: Introduction to the DE2-70 FPGA boardGroup formation 6:30pm;Wed 1/29L7: DAC, oversampling and equalization, digital filter designHW3 due4Lab 4: DSP on the DE2-70 FPGA boardHW3 due4Lab 5: FIR and IIR filter design, roundoff and overflowWed 2/5Wed 2/5L9: Filter design continuedFri 2/7Fri 2/7Project proposal due at 5pmby email5Lab 5: IIR filtersMon 2/10Wed 2/12Project proposal presentations 10:30-12HW4 dueFri 2/14Project proposal presentations 10:30-12HW4 dueFri 2/14Project proposal presentations 5-7pmRoom 10056Lab 6: Interrupts, FFT and graphicsMon 2/17L10: DFT and FFT, spectral leakageWed 2/19L11: More on DFT, windowingHW5 due; parts order due7Optional Lab 7: SPI, 12S or video processingMon 2/26No lecture (midterm study); Midterm 5-7pm in DOW 1013Backup time Friday 5-7pm8No classes; winter break9Mon 3/10L13: Guest lecture on CoE Center for EntrepreneurshipWed 3/19No lecture; Milestone I meetings4419 EECS<	
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11 Mon 3/24 L15: Filter noise, oversampling	
Wed 3/26 L16: Noise shaping, sigma-delta ADC and DAC	
12 Mon 3/31 L17: Making poster; transfer function measurement	
Wed 4/2 No lecture	
13 Mon 4/7 No lecture	
Wed 4/9No lecture; Milestone 2 meetings4419 EECS	
14 Mon 4/14 No lecture: prep for Design Expo	
Thu 4/17 CoE Design Expo	
15 Tue 4/22 Final project presentation 5-10pm 1311 EECS	
Thu 4/24 Final project report due by 5pm by email	