## EECS 473 Midterm Exam

## Fall 2021

Name: $\qquad$ unique name: ___Key $\qquad$
Sign the honor code:
I have neither given nor received aid on this exam nor observed anyone else doing so.

## NOTES:

1. Closed book and Closed notes
2. There are $\mathbf{1 2}$ pages total for the exam as well as handouts which you will need for the last question.
3. Calculators are allowed, but no PDAs, Portables, Cell phones, etc. Using a calculator to store notes is not allowed nor is a calculator with any type of wireless capability.
4. You have about 120 minutes for the exam.
5. Though the last question is worth significantly less than half the points, we expect it will take at least half of the exam time.

Be sure to show work and explain what you've done when asked to do so. That may be very significant in the grading of this exam.

1. Circle the letter in front of all the true statements. [8 points, -2.5 per wrong circle/lack of a circle, minimum 0] True answers are in red.
a) Compared to the PCB power/ground plane, a bypass capacitor typically has a higher capacitance, ESR, and ESL.
b) An advantage of RM scheduling compared to EDF scheduling is that RM scheduling has static priorities, often making it easier to implement.
c) When designing a power distribution network, very low frequency noise (say 1 KHz ) is primarily handled by the power supply.
d) The "L" in "LDO" indicates that the output voltage has to be considerably lower than the input voltage.
e) gcc (the GNU C compiler) should be avoided in commercial software because it is licensed under the GPL and thus any code generated by it must also be licensed under the GPL.
f) Alkaline batteries are a common type of primary-cell batteries while lithium-polymer batteries are a common type of secondary-cell batteries.
2. Multiple choice—write letter in blank. [8 points, -2.5 per wrong/blank answer, minimum 0]
a) If you have 2 capacitors that have $2 \mu \mathrm{~F}$ capacitance, $4 \mathrm{~m} \Omega$ resistance and 8 nH inductance and put them in parallel, then you would expect that together they would have $\qquad$ -.
a. $4 \mu \mathrm{~F}, 2 \mathrm{~m} \Omega, 16 \mathrm{nH}$
b. $1 \mu \mathrm{~F}, 2 \mathrm{~m} \Omega, 16 \mathrm{nH}$
c. $4 \mu \mathrm{~F}, 2 \mathrm{~m} \Omega, 4 \mathrm{nH}$
d. $2 \mu \mathrm{~F}, 8 \mathrm{~m} \Omega, 8 \mathrm{nH}$
b) On a PCB, traces on to different layers of a board are typically connected by a $\qquad$ .
a. via
b. trace
c. LDO
d. capacitor
e. multiplexer (MUX)
c) On a $\mathrm{PCB}, 40$ mils is $\qquad$ 40 mm .
a. greater than
b. less than
c. equal to
d) Priority inversion is where $\qquad$
a. A semaphore or other lock is used to lock a resource and that lock is not released when it should be.
b. A semaphore or other lock is used with " 1 " being free and " 0 " being not-free, rather than the other way around.
c. A high priority task is blocked by a medium priority task due to the high-priority task sharing a semaphore or other lock with a low priority task.
d. Priority inheritance sets the high-priority task to the lowest possible priority.

## 3. Scheduling [9 points]

Say you have the following groups of tasks. For each group find the CPU utilization and identify which groups are RM and which are EDF schedulable. Indicate if you needed to do the critical instant analysis. If needed, clearly show that analysis. The following equation may prove useful.

$$
\sum_{i=1}^{n} U \leq n\left(2^{1 / n}-1\right)
$$

| Group | T1 <br> Execution <br> Time | T1 <br> Period | T2 <br> Execution <br> Time | T2 <br> Period | T3 <br> Execution <br> Time | T3 <br> Period | $\%$ <br> Utilization |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | 1 | 4 | 3 | 6 | 1 | 5 | 0.95 |
| B | 1 | 10 | 4 | 12 | 1 | 4 | 0.683 |
| C | 2 | 5 | 2 | 6 | 3 | 11 | 1.006 |


| Group | EDF <br> Schedulable? | RM <br> Schedulable? | Did you need to examine <br> the critical instance? |
| :---: | :---: | :---: | :---: |
| A | Y | N | Yes |
| B | Y | Y | No |
| C | N | N | No |


|  | $0-1$ | $1-2$ | $2-3$ | $3-4$ | $4-5$ | $5-6$ |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| T1 |  |  |  |  |  |  |  |  |  |
| T2 |  |  |  |  |  |  | Failed |  |  |
| T3 |  |  |  |  |  |  |  |  |  |

## 4. Linux device drivers [10 points]

Consider the following code found as the read function member of the file_operations struct for a Linux kernel module. It is associated with the device file "/dev/txx2" (so a read of the file /dev/txx2 will result in this function being called). Assume that everything is set up appropriately beforehand. Ignore the fact that copy_to_user's return value is being ignored (it's just a warning...).

```
const char s[] = "ABCDEFGHIJKLMNOPQRSTUVWXYZ";
ssize_t memory_read(struct file *filp, char *buf,
                    size_t count, loff_t *f_pos) {
        /* Transferring data to user space */
        /* Changing reading position as best suits */
        if(*f_pos>=4)
            return 0;
        *f_pos+=2;
        printk("<1> fpos= %d\n",*f_pos);
        return 4;
    }
```

Say that someone does a cat of / dev/txx2.
a) What will appear in the log file? [4]
<1> fpos=2
<1>fpos=4
b) What will be printed by the cat command? [6] ABCDCDEF
5. Decoupling capacitors [6 points]


The above graph shows the frequency vs. impedance for a given capacitor. Redraw the graph showing the same information we instead put used 10 new capacitors (in parallel) which each had the same ESR and ESL but only $1 / 10^{\text {th }}$ the capacitance.

Line isn't great, but basic idea is that it should flatten around 0.002 and start going up at 1 E 7 .

## 6. Batteries [10 points]

Consider the following battery discharge curves from a real battery specifications. Clearly justify your answers.

a) About how long would you expect the above battery to be able to drive 6 V with a 100 mA load? [4]

Looks like $\sim 0.11 \mathrm{mAh}$ capacity at 6 V , so at 100 mA that's $0.11 \mathrm{mAh} / 100 \mathrm{~mA} \sim=4 \mathrm{~s}$. As long as your work is clear and correct, we'll take answers near that.

b) About how long could a fresh CR2032H battery drive a $1 \mathrm{k} \Omega$ load that requires at least 2.5 Volts? (Notice the typo in the $1^{\text {st }}$ graph on the $y$-axis!). [6]

Could do this a number of ways.

- Looks like $\sim 145 \mathrm{mAh}$ capacity. Current appears to be close to $2.5 \mathrm{~V} / 1000 \Omega=2.5 \mathrm{~mA}$. So $\sim 58$ hours.
- At $15 \mathrm{k} \Omega$ you get $\sim 1250$ hours (graph 2 ). At $1 \mathrm{k} \Omega$ looks like you get 145 rather than 240 $m A h$. So capacity loss*extra current drain rate*original time $(145 / 240)^{*}(1 / 15) * 1250=\sim 50$ hours.
Pretty much anything in the 45-65 hour range seems reasonable as long as the work is clear and reasonable.


## 7. Linear regulators [6 points]

You have a linear regulator with a 9 V input, a 5 V output, and a quiescent current of 5 mA . If the load being driven by the regulator is a constant 100 Ohms, how much power is wasted by the regulator? You must clearly show your work.
$\mathrm{P}=\mathrm{IV}$. $\mathrm{V}=\mathrm{IR}$, so $\mathrm{P}=\mathrm{V}^{2} / \mathrm{R}$.

P doing useful work is $(5 \mathrm{~V})^{2} / 100 \Omega=.25 \mathrm{~W}$.
Current in is $5 \mathrm{~V} / 100 \Omega+5 \mathrm{~mA}$ for quiescent or $=55 \mathrm{~mA}$.
Total power in is $55 \mathrm{~mA}^{*} 9 \mathrm{~V}=0.495 \mathrm{~W}$. So wasted $=0.245 \mathrm{~W}$.
Same thing, slightly different math:
Current to the load is $5 \mathrm{~V} / 100 \Omega=50 \mathrm{~mA}$. So used power is $50 \mathrm{~mA}^{*} 5 \mathrm{~V}=0.25 \mathrm{~W}$.
(rest is the same).
8. Short answer [5 points]

Briefly define the term "power integrity" and how we achieve it on a PCB.
Power integrity is about keeping a constant voltage between the power and ground of an IC in the face of various forms of noise, including variations in the current needs of the IC. We do this mostly by adding capacitors as needed to maintain that voltage difference. We may use different sizes of capacitor to be able to respond well to different frequencies of noise.

## Problem X: Design problem [38 points]

## You should read the entire question before starting.

In the harsh conditions of space, electrical power is a precious resource that needs to be monitored carefully. Satellites made to orbit the earth almost always include an Electrical Power System (EPS) that will monitor, regulate, and control the electrical power to the satellites components. But some of these components require a lot of power, and potentially overheat the batteries or harm the power supply and thus cause a mission failure. The EPS is present to protect the integrity of the batteries and power supply.

You have been tasked to design an early prototype for a satellite EPS. This means assembling the hardware as well as writing the associated firmware. The system has the following components:

1. One Arduino Uno Board
2. One Current Sensor chip - INA219 in a 8-Pin SOT-23 package: This chip measures the voltage across a $100 \mathrm{~m} \Omega$ shunt resistor, and uses Ohm's Law ( $I=V / R$ ) to calculate the current. An abbreviated version of the data sheet has been provided.

3. One Power Distribution Switch - TPS2013A: This switch is low impedance and can pass a large amount of current. A very abbreviated version of the data sheet has been provided.
4. One Thermistor (temperature dependent resistor):

Assume that this thermistor is linear, and is $5 \mathrm{k} \Omega$ at $0^{\circ} \mathrm{C}$ and $2 \mathrm{k} \Omega$ at $80^{\circ} \mathrm{C}$. It should be connected in the following configuration with a $3 \mathrm{k} \Omega$ resistor to measure the temperature of the battery.

5. One Lithium Ion Battery with an output voltage of 3.7 V
6. One pre-built Boost DC-DC converter with an output voltage of 5 V . Assume an ideal converter with no output noise (basically, it's magic).
7. A high-power ( $\max 5 \mathrm{Amp}$ ) LED with a Potentiometer to simulate a variable load.
8. Any other passives you may require.

The system should have the following characteristics:

1. The batteries should provide power to the 5 V voltage regulator, which provides a stable voltage for the rest of the system.
2. The LED with a Potentiometer will simulate the other components of the satellite (such as other computers, reaction wheels, thrusters, RF transceivers, etc.). The LED and potentiometer should be connected so that the potentiometer can control the brightness of the LED.
3. The Arduino should be able to monitor the current flowing through the potentiometer and LED. If the current exceeds $\mathbf{1 0 0 0} \mathbf{m A}$ of current, the Arduino should switch off the power to the LED for 5 seconds. After those 5 seconds, the power should be turned back on and the current should be monitored again. You should do this measurement at least once per second. There is no need to take multiple measurements and it's fine if your shutdown current is within a few mA of 1000 .
4. The Arduino should also monitor the temperature of the batteries that heat up due to power being and solar radiation. If the temperature of the batteries exceeds $40^{\circ} \mathrm{C}$ the power to the LED should be switched off until the temperature reaches $35^{\circ} \mathrm{C}$ or below.

Part A: Wiring [10 points]
Provide the connections between the different components of the system. You should also provide power and GND to all components. Add resistors and capacitors as needed. You may use labels to make connections.


Can also potentially add bypass caps,
for power integrity but not strictly necessary.

## Part B: I2C interfacing [5 points]

Write two functions that will let you write and read registers on the INA219:

```
void ina219_write(uint8_t addr, unit16_t value) {
    //I2C address depends on wiring in part A
    Wire.beginTransmission(0x40);
    Wire.write(addr); //send register address first
    Wire.write((value >> 8) & 0xFF); //Write high byte
    Wire.write(value & 0xFF); // Write low Byte
    Wire.endTransmission(); //Sends everything out
}
int ina219_read(uint8_t addr) {
    //I2\overline{C}}\mathrm{ address depends on wiring in part A
    Wire.beginTransmission(0x40);
    Wire.write(addr); //send register address first
    Wire.endTransmission();
    Wire.requestFrom(0x40, 2); //Request 2 bytes
    while(Wire.available() < 2); //Wait for 2 bytes
    return ((Wire.read() << 8) | Wire.read())
}
```

Part C: Short answer [6 points]
Use formulas 1 and 2 on page 12 of the INA219 data sheet to answer the following questions:

1. What would be a reasonable value to use as the "Maximum expected current"? Briefly justify your answer.
Can be anything greater than 1A, but less than 5A. Technically the TPS switch is only rated for 1.5A continuous, and the INA cannot measure more than 3.2A a that shunt resistance (as 320 mV is the maximum measurable value). The easiest value would be 3.2768 A , since that would cancel well with $2^{\wedge} 15$ in the next equation.
2. Given your answer to part a, what would be the value of Current_LSB?

Current_LSB $=\frac{3.2768 \mathrm{~A}}{2^{15}}=0.1 \mathrm{~mA}$
(Lots of potential numbers but rounding to a good number would probably be the best thing to do).
3. Compute the value "Cal". Show your work.

$$
\text { Cal }=\operatorname{trunc}\left(\frac{0.04096}{0.0001 A * 0.1 \Omega}\right)=4096\left(\frac{1}{V} \text { This is where units are important }\right)
$$

Part D: Setup [6 points]
Write a setup function that will initialize any inputs, outputs, and sensors you may need. The setup function should also enable the LED output. Define any variables that you may need.

```
#define I2C ADDR 0x40
#define EN 2
#define THERM A3
#define CONFIG REG 0x00
#define SHUNT_REG 0x01
#define BUS_REG 0x02
#define POWER_REG 0x03
#define CURRENT REG 0x04
#define CALIB_REG 0x05
void setup() {
    Wire.begin(); //Initialize I2C
    pinMode(EN, OUTPUT); //Initialize GPIO
    //Optionally Configure INA (default config is good)
    //Just be aware of changes to PGA gain and shunt
    //measurement mode
    ina219_write(CONFIG_REG, 0x399F);
    //Set the calibration on the INA based on part C
    //Must be 16 bits or less.
    ina219_write(CALIB_REG, 4096);
    digitalWrite(EN, 0); // Drive low to enable output
}
```


## Part E: Loop [10 points]

Write the loop function for the system. This function should monitor the temperature and the current, and control the power switch depending on the status of the system. Define any variables or helper functions you may need.

Lots of ways to do this that are more elegant; this is probably one of the simplest ways that still technically meets the requirements.

```
void loop() {
    //Check if current is greater than 1A.
    if (get_current_mA() > 1000.0) {
        digitalWrite(EN, 1);
        delay(5000); //Turn off for 5 seconds
        digitalWrite(EN, 0);
    }
    //Check Temperature of Battery
    uint16_t temp = analogRead(THERM);
    // 40C : 3.5kOhm -> 5V*3/(3+3.5) = ~ 2.3V
        1024*3/6.5=~473
    // 35C : ~ 3.6875kOhm -> 5V*3/(3+3.6875) = 2.243V
        1024*3/(3+3.6875)=~459
    if (temp > 472) {
        digitalWrite(EN, 1);
    } else if (temp < 459) {
        digitalWrite(EN, O);
    }
    delay(500); // < 1 second intervals, not exact.
}
float get_current_mA() {
    //LSB is 0.1mA. Will need to match value to 1A.
    return ((float) ina219_read(CURRENT_REG))/10; //mA
}
//Technically don't even need to set calibration
float get_current_mA_altern() {
    //current = shunt_voltage / shunt resistor
    //LSB of shunt voltage is 0.010mV & shunt is 0.10hms
    return ((float) ina219_read(SHUNT_REG))/10; //mA
```

