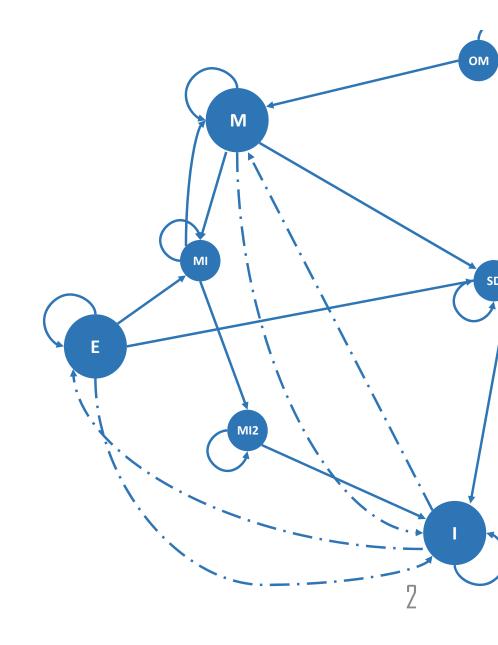


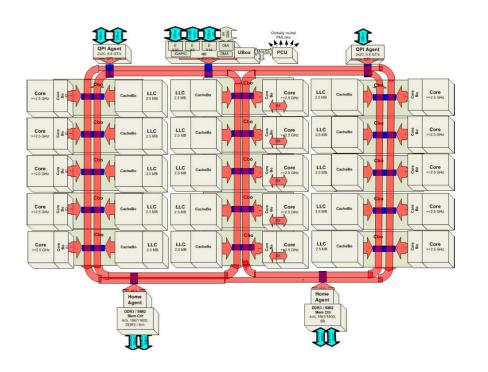
Amlan Nayak & Jay Zhang

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- Motivation
- Background
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Issue with Coherence Protocols



Difficult to automatically verify for many core systems

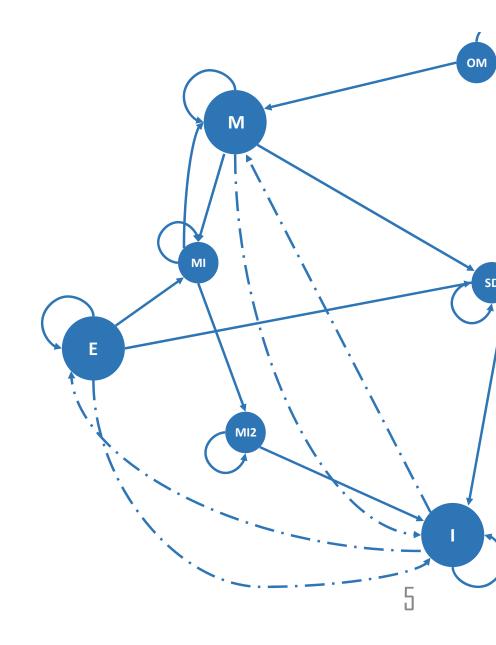
Better performance \Rightarrow Complex protocols \rightarrow Difficult to formally verify

GOAL

Architect arbitrarily large flat protocols such that they can be verified using a mostly-automated methodology

Overview

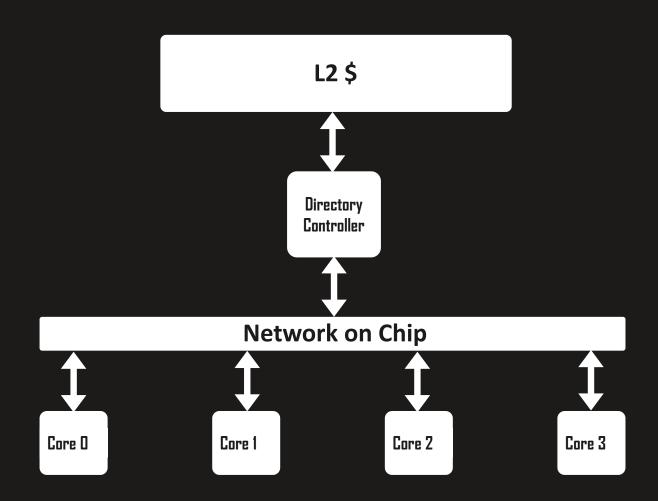
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Coherence Protocols

Two Primary Categories: Snooping & Directory-based

(ex. MSI, MESI, MOESI, MESIF)



Formally verifying a coherence protocol Theorem Proving

State Space Exploration (with Murphi)

GOAL

Architect arbitrarily large flat protocols such that they can be verified using a mostly-automated methodology

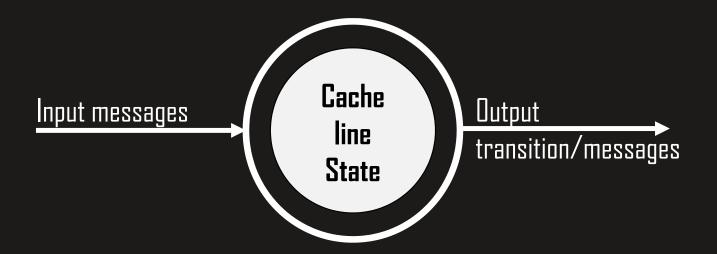
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Model Protocol in Murphi Check invariants

Model Protocol in Murphi

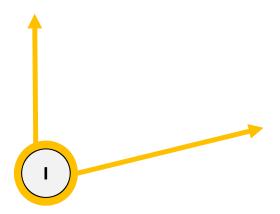
Murphi Processor Node State Definition



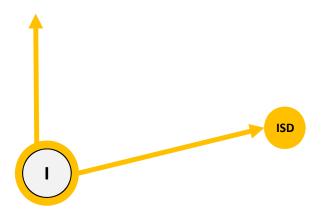
MOESI protocol Processor Cache Controller



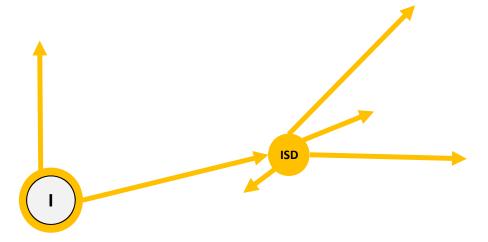
MOESI protocol Processor Cache Controller

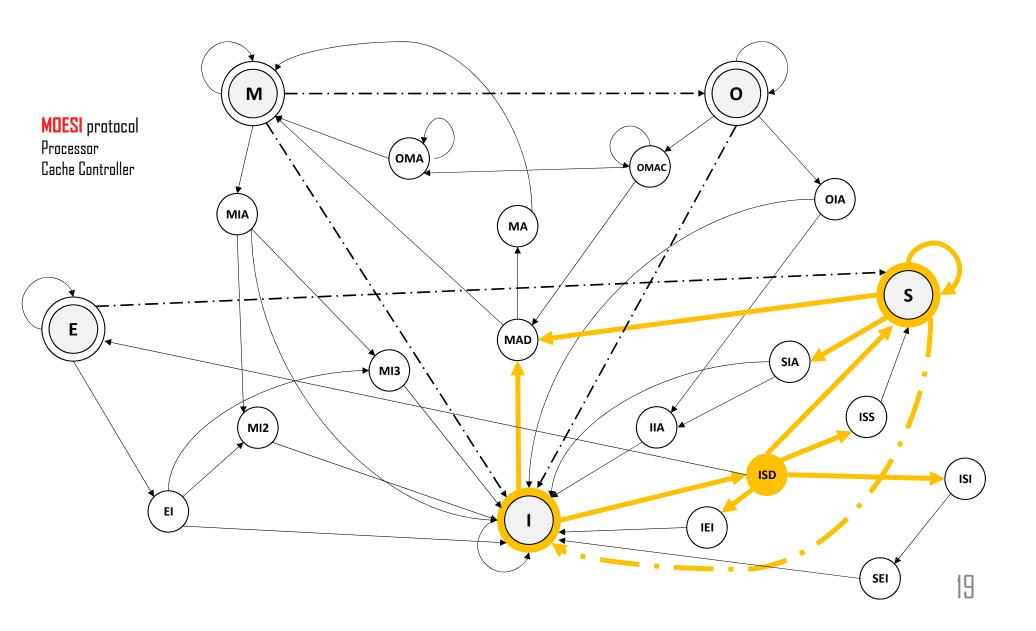


MOESI protocol Processor Cache Controller



MOESI protocol Processor Cache Controller



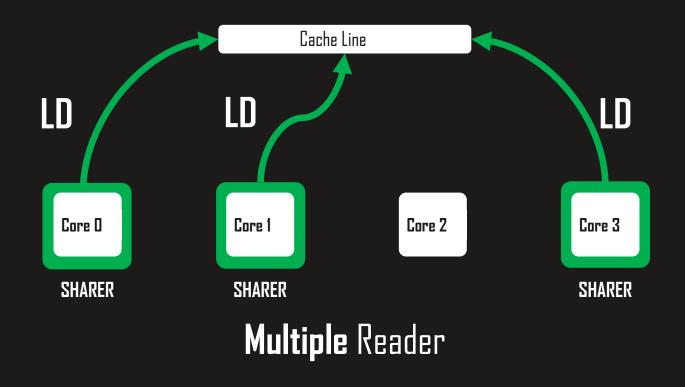


Model Protocol in Murphi Check invariants

- 1. Permission Invariant: Single-Writer, Multiple-Reader
 - 2. Data Invariant: Read returns value of last write

- 1. Permission Invariant: Single-Writer, Multiple-Reader
 - 2. Data Invariant: Read returns value of last write

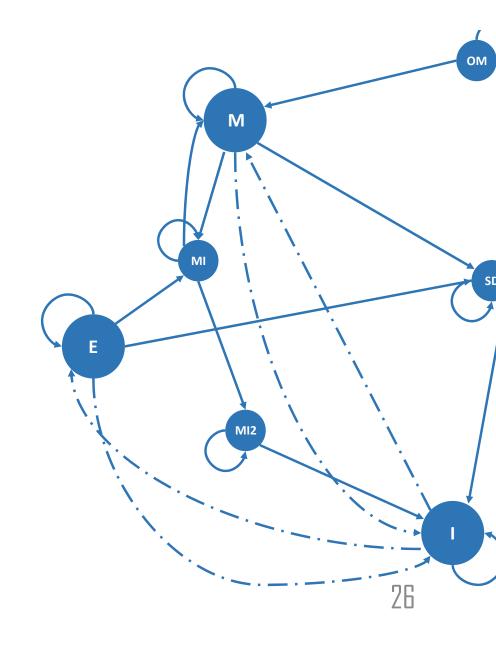




- 1. Permission Invariant: Single-Writer, Multiple-Reader
 - 2. Data Invariant: Read returns value of last write

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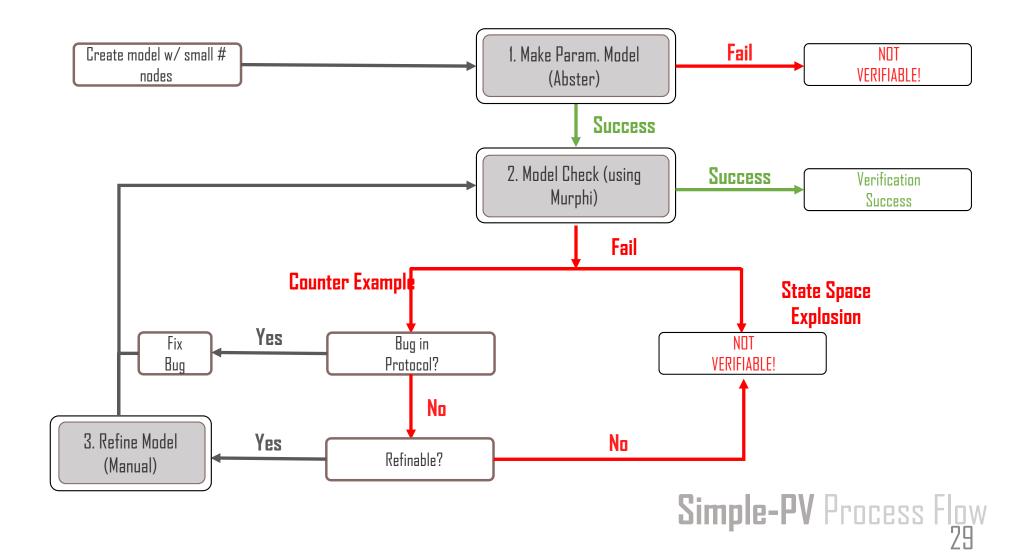
PARAMETRIC VERIFICATION (PV)

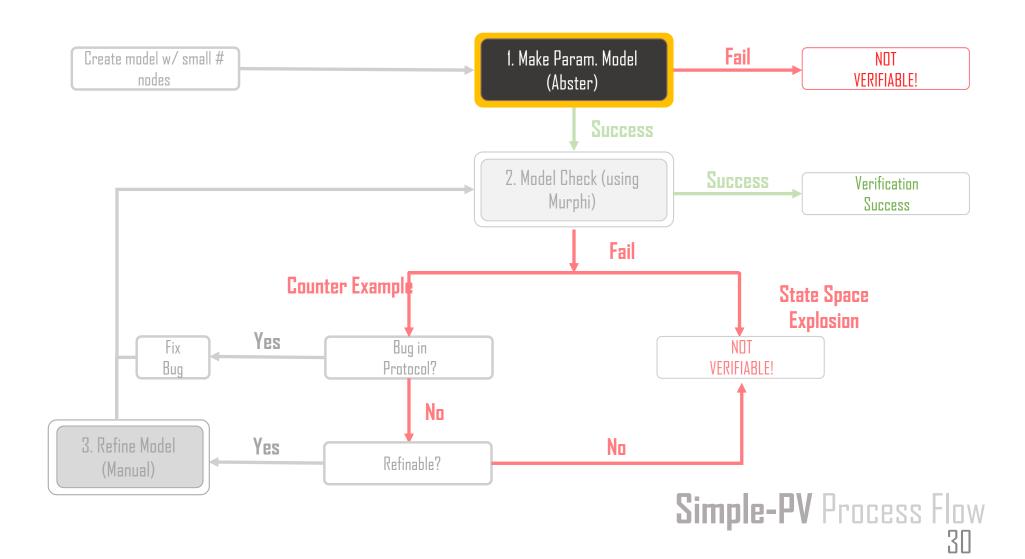
Treat number of *nodes* as a **parameter (using Abster tool)**Prove properties of design agnostic to **parameter size**

This process scales to many nodes and is almost fully automatic

Simple-PV Process Flow

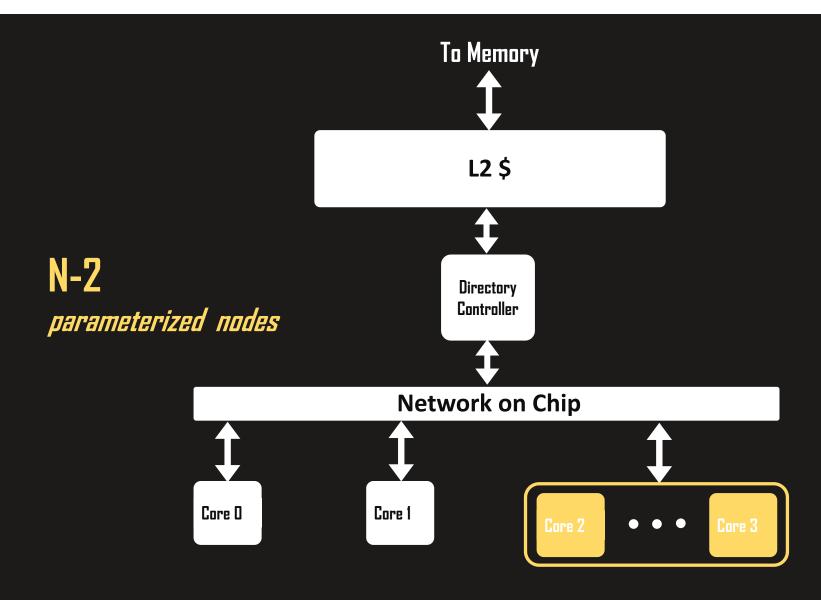
How to design a readily verifiable Coherence Protocol

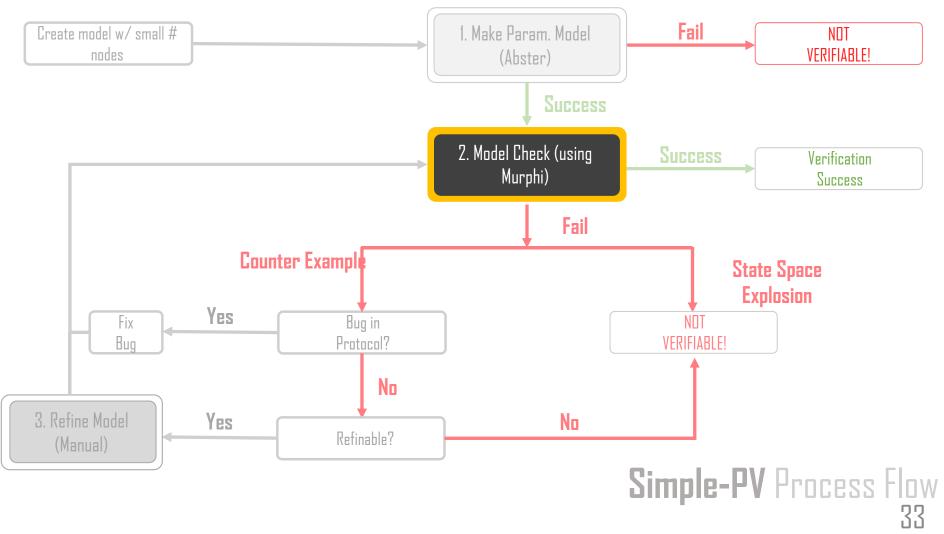




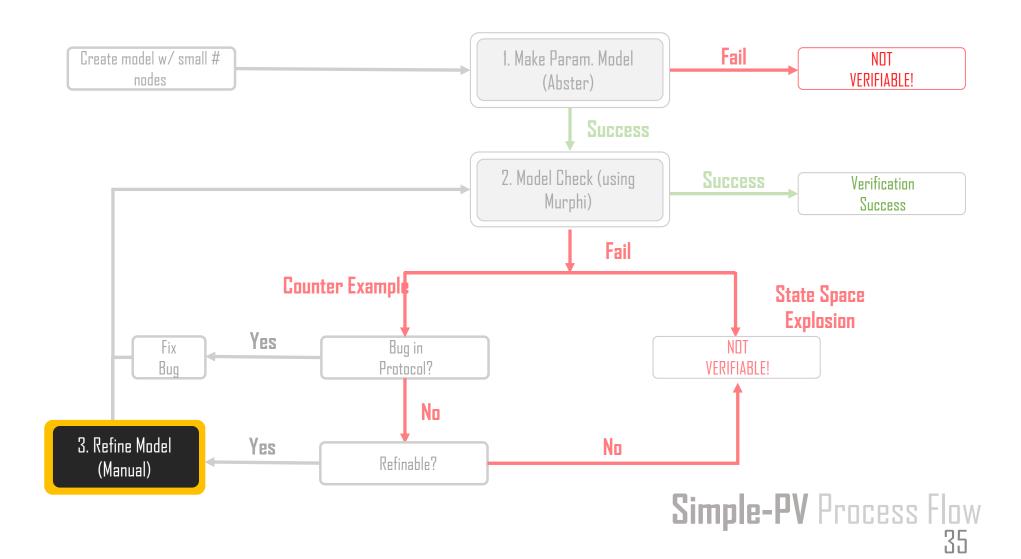
Automatically Create Parametric Model

- Create parametric model from non-parametric model
 - N concrete nodes -> 2 concrete nodes + "Other Node" (N-2)
 - Abster automatic abstraction tool
- Abster over-approximates the behavior of the N-2 nodes
- If Abster fails, modify protocol until it is compatible





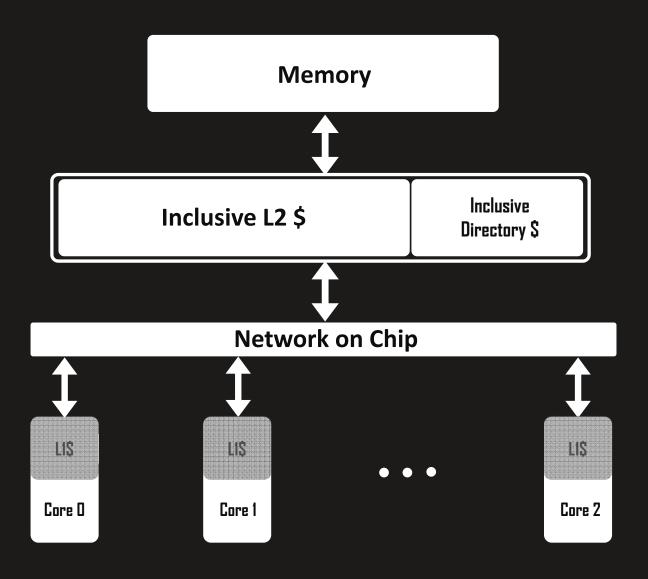
Automatically Model Check the model (MURPHI)



Manually Refine the Model

- Over-approximation leads to spurious invariant violations
- Must modify behavior of "Other node"
- KEY: Add constraint and check their validity
 - Add invariant (*lemma*) must be true for non-abstracted model
 - Check on the concrete nodes

System Architecture for PVCoherence





Network on Chip VCO Requests VC1 Responses VC2 Forwards

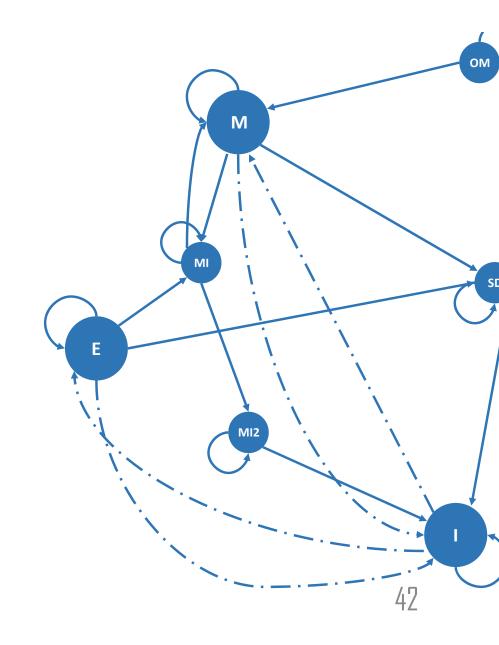
Objective



during **protocol design**

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for Simple-PV compliance

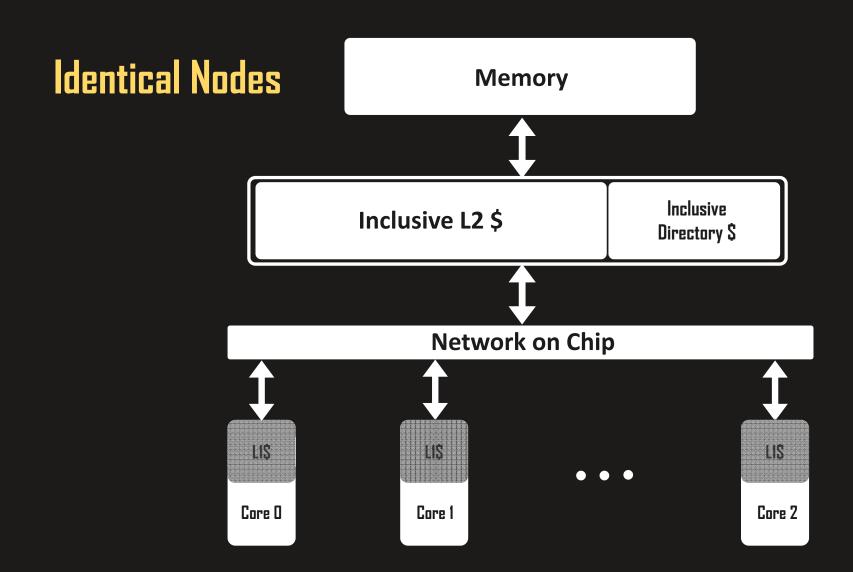
for Simple-PV compliance

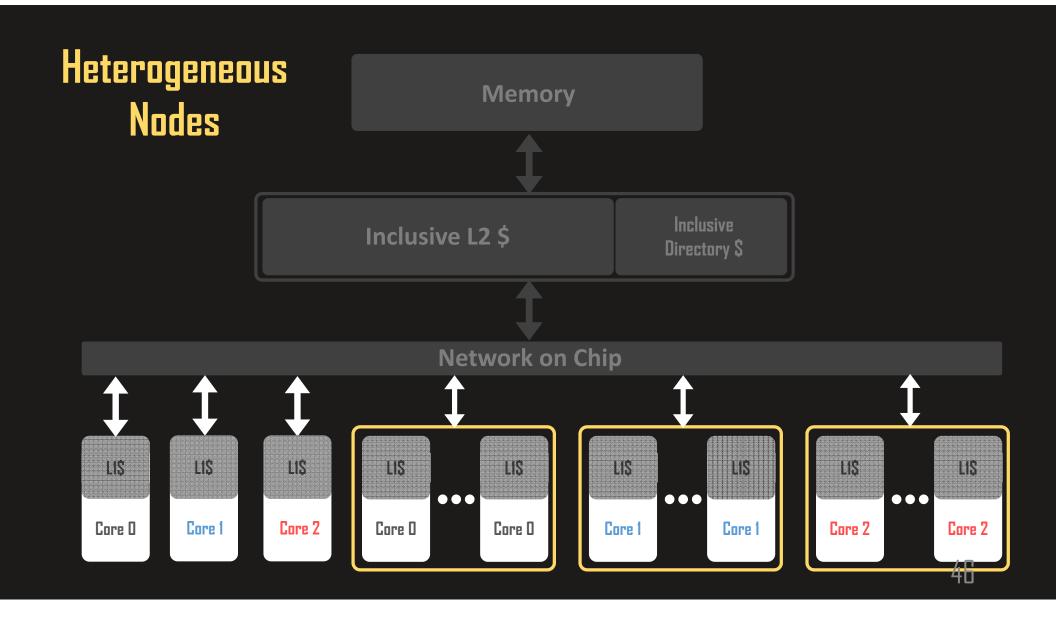
#1: Identical Nodes

#2: No variables must depend on number of Nodes

#3: No ordering over list/queue sized by number of nodes

#4: Should not parameterize buffers/queues in more than 1-dim.





for Simple-PV compliance

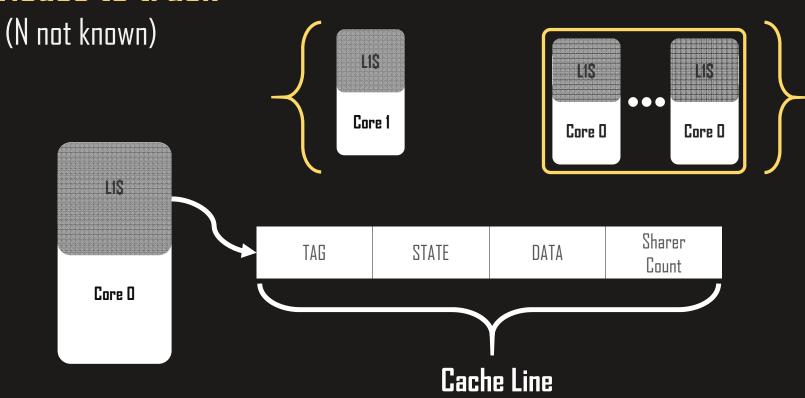
#1: Identical Nodes

#2: No variables must depend on number of Nodes

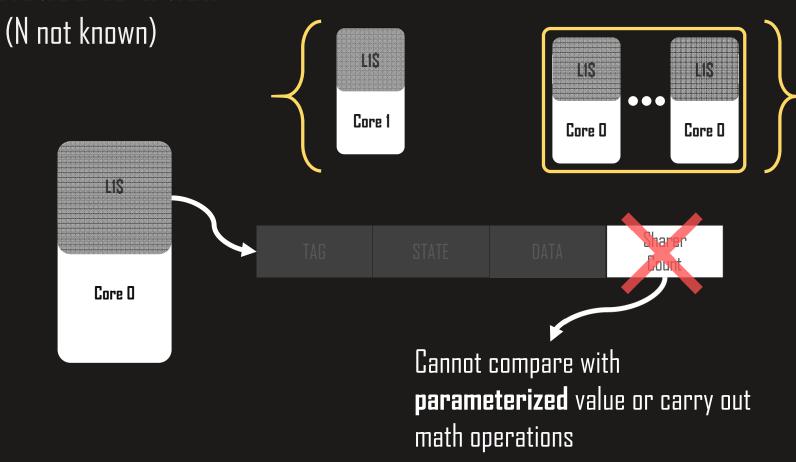
#3: No ordering over list/queue sized by number of nodes

#4: Should not parameterize buffers/queues in more than 1-dim.

N-1 Nodes to track



N-1 Nodes to track



N-1 Nodes to track (N not known) LIŞ 118 LIS ••• Core 1 Core O Core O LIS Sharer Set וטטל Core O Replace with sharer set

(bit vector)

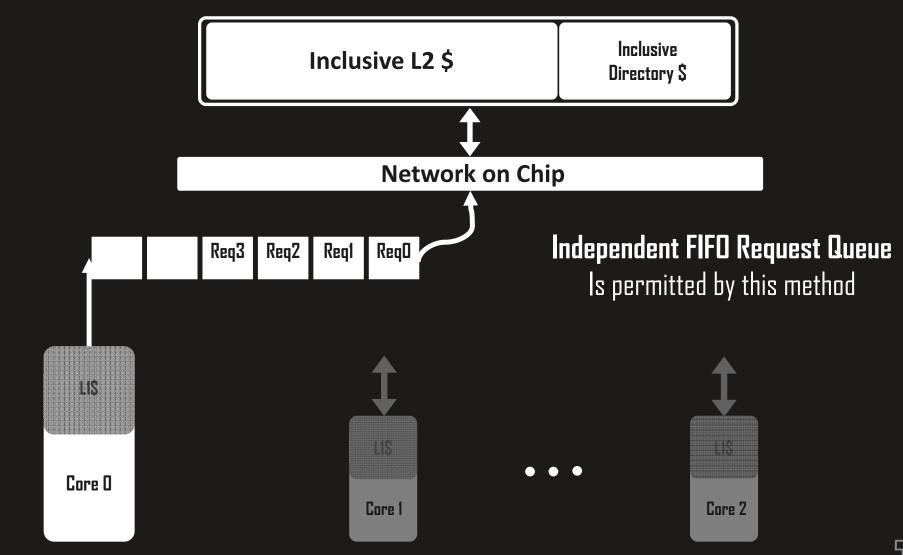
for Simple-PV compliance

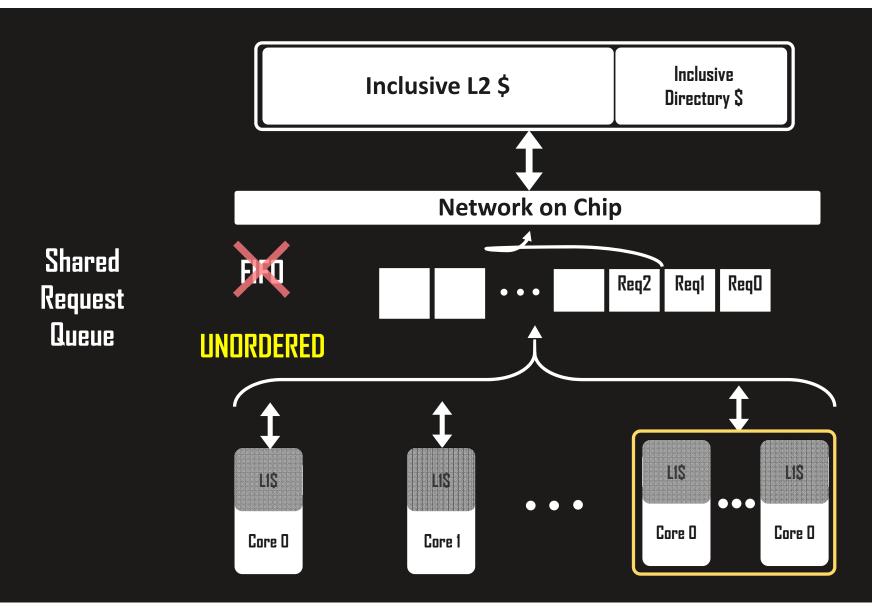
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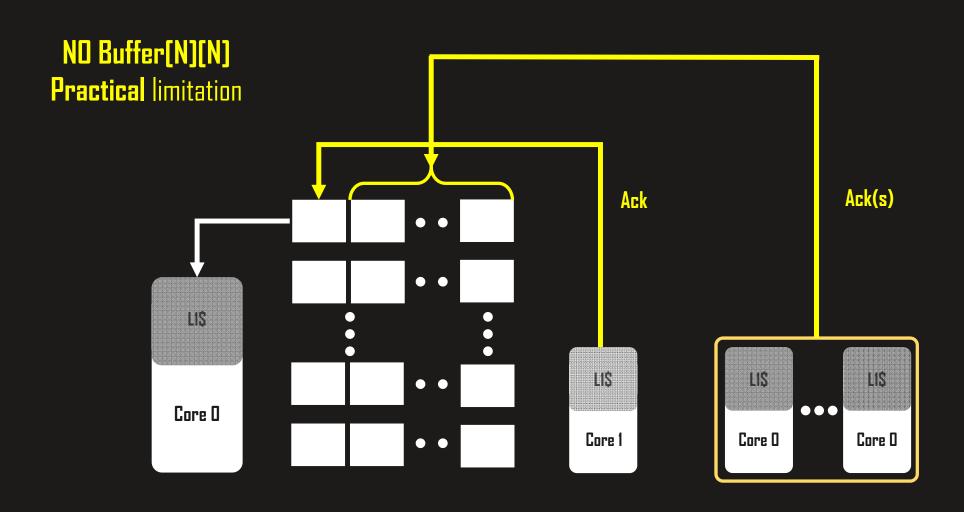
for Simple-PV compliance

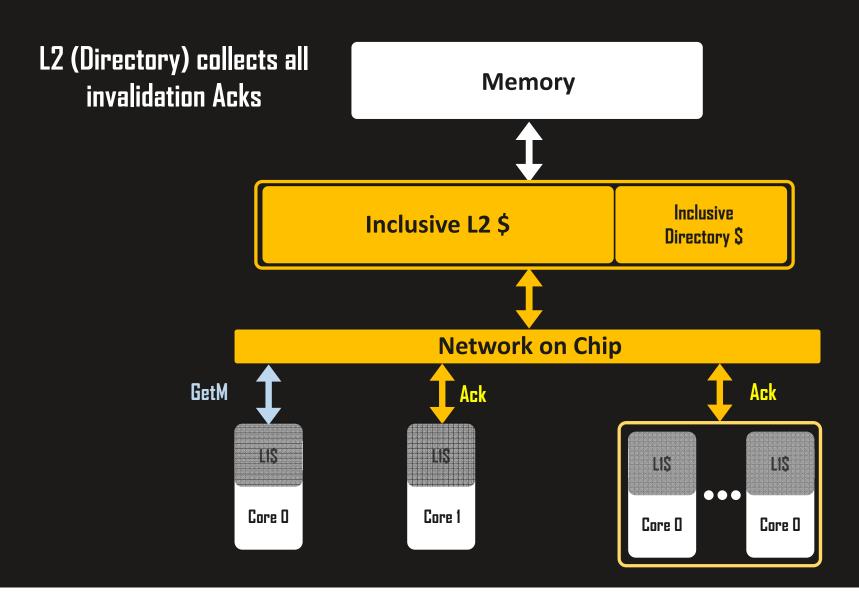
#1: Identical Nodes

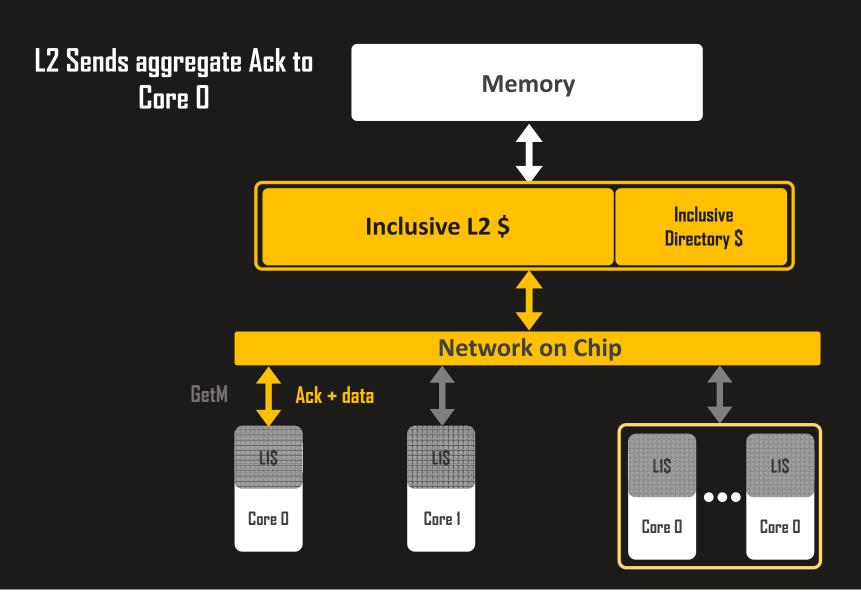
#2: No variables must depend on number of Nodes

#3: No ordering over list/queue sized by number of nodes

#4: Should not parameterize buffers/queues in more than 1-dim.

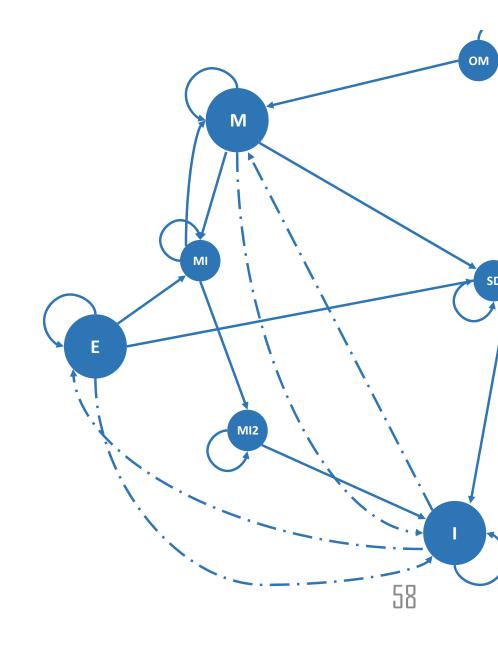






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Optimizations (OP-MOESI)

Optimization	Compatible with SimplePV?	Impact
Adding Exclusive State	Υ	NO IMPACT
Adding Owned state	Υ	Add 2 lemmas
Adding Self-Upgrade	Υ	Add lemma
Adding Silent Evictions	Υ	Add lemma
Removing the completion messages for GetS when data response comes from L2\$	Υ	Add lemma
Removing the completion messages for GetM	N	

OP-MOESI to **PV-MOESI**

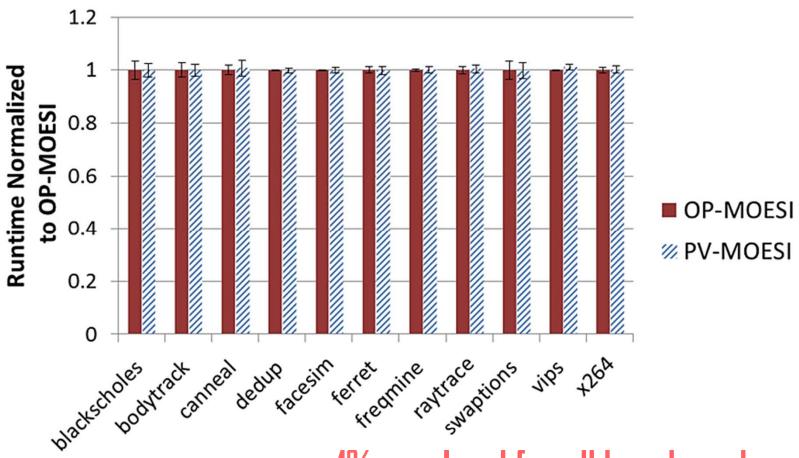
- To ensure successful abstraction by Abster...
 - For GetM
 - Replace response **counter** with **sharer set**
 - Let L2 collect Acks and send aggregated Ack to requesting L1
 - Remove point-to-point ordering in all VCs and avoid races by adding extra messages or transient states but without blocking L1

OP-MOESI to **PV-MOESI**

- To ensure successful verification by Murphi...
 - Problem: multiple in-flight GetM requests without ordering
 - Solution: L2 blocks other subsequent requests whenever it receives a GetM request until it receives a Completion message from the requesting L1
 - Impact: performance decrease due to blocking at L2

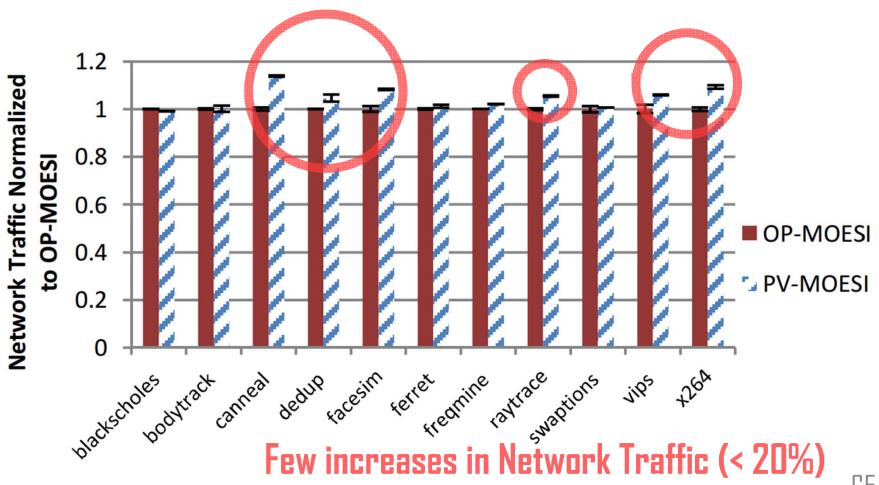
Evaluation: OP-MOESI vs. PV-MOESI

Runtime

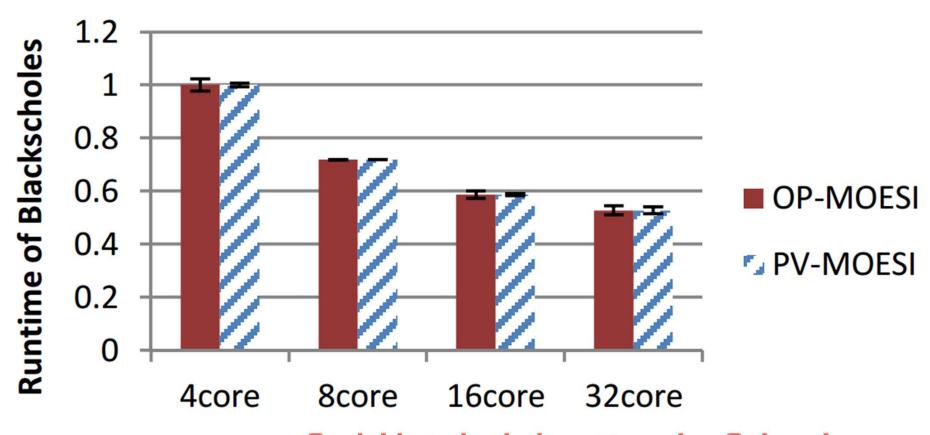


< 1% overhead for all benchmarks

Network Traffic Overhead



Performance Scalability



Scalable in both directions (up & down)

Storage Overhead



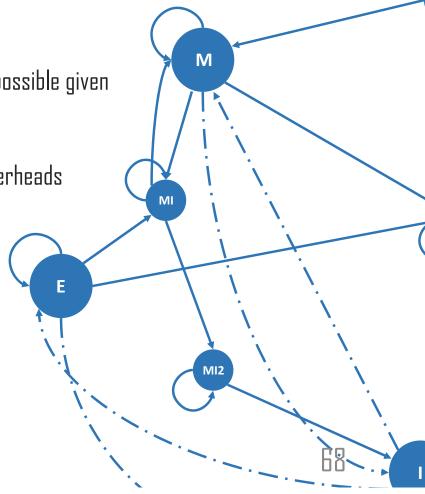
Overhead is generally **negligible**

CONCLUSION

 Design of parametrically verifiable coherence protocols is possible given that the guidelines introduced here are adhered to

• There is no significant performance drawbacks or storage overheads

Automation is key



Debate

• Is it necessary for a protocol to be parametrically verifiable? There are a few design corners that are cut to make such PV-compliant protocols work. Is this worth it?