Perform conceptual and detailed transistor level IC design of Power Management Integrated Circuits (PMIC) functions. Experience with Switched Mode Power Supplies (SMPS) is desired. Experience with LDOs (low-dropout voltage regulators), Bandgaps, Power FETs, Logic Design, D2As is a beneficial, as well as experience with radio circuits and systems, esp. RF Power Amps.

Collaborate with product line engineers to architect/define/specify PMIC requirements to work with power amp and other radio products.

Use circuit simulation tools (primarily Cadence) to design these commercial IC's to meet the agreed upon requirements.

Perform a layout of the design for physical description of how the IC will be manufactured in the wafer fabrication facility. Also, direct layout designers to do the same.

Perform test and evaluation of prototype PMICs to determine if the IC has met performance requirements and simulation predictions.

Communicating with the product line engineers is a must. This is done through presentations of your work, attending appropriate team meetings, documenting your work....  Assist with the transfer of the successful design to product lines and/or operations for manufacture.

BSEE is required. MSEE is preferred.