



# VLSI for Signal Processing and Communication Systems

**3 credits**

**Fall 2017 - EECS 598 Section 005**

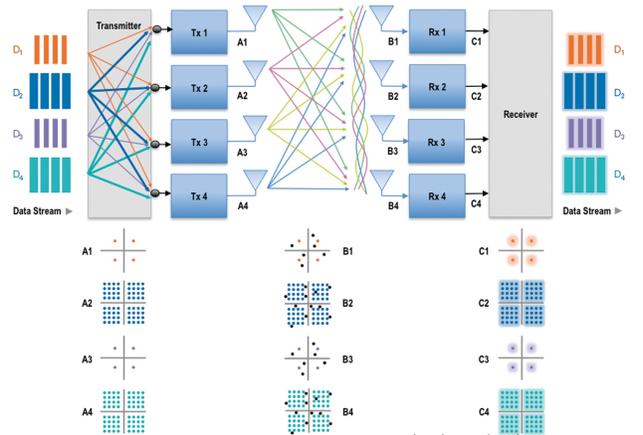
**Instructor: Professor Hun-Seok Kim**

The advance in very-large-scale integration (VLSI) technologies has enabled intelligent mobile internet-of-things (IoT) applications that are now integral parts of our daily lives. Recently emerging image/audio signal processing, machine learning, and wireless communication systems constantly impose new challenges on VLSI integrated circuit (IC) implementations. These implementations must satisfy stringent latency, throughput, and energy-efficiency constraints that are critical to ubiquitous IoT applications.



Source: tradeshift blog

This course will survey methodologies to design energy-efficient and/or high-performance VLSI systems for the state-of-the-art image/audio processing, machine learning, and wireless communication systems. The primary focus of the course is on designing hardware-efficient algorithms and energy-aware VLSI IC architectures to deliver the performance and efficiency required by various signal processing applications. The course will be a mix of lectures and student-led presentations/projects. The content will be suitable for senior undergraduates or graduate students interested in hardware-efficient signal processing algorithms and their VLSI implementations.



Source: wirelessdesignmag.com

**Questions: Please email Prof. Kim at [hunseok@umich.edu](mailto:hunseok@umich.edu)**