

# A Novel Parametric-Effect MEMS Amplifier

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## Abstract

This paper presents the theory and measurements of a mechanical parametric effect amplifier with a 200 kHz input signal and a 1.84 MHz output signal. The device used is a MEMS time-varying capacitor which is composed of an array of low-stress metallized silicon-nitride diaphragms, and is pumped by a large signal voltage at 1.64 MHz. This induces a large change in the capacitance, and results in parametric amplification of an input signal at 200 kHz. The parametric amplifier capacitance is 500 pF resulting in an output impedance of 140  $\Omega$ . A higher impedance can also be achieved with a lower capacitance. To our knowledge, this device is the first-ever MEMS mechanical up-converter parametric-effect amplifier developed with an up-conversion ratio of 9:1. The measurements agree very well with theory, including the effect the series resistance and the Q of the MEMS time-varying capacitor. The application areas are in amplifiers which operate at very high temperatures (200-600  $^{\circ}\text{C}$ ), under high particle bombardment (nuclear applications), in non semiconductor-based amplification, and in low-noise systems since parametric amplifiers do not suffer from thermal, shot or  $1/f$  noise problems.

## I. INTRODUCTION

Parametric-effect devices, based on non-linear reactances such as capacitors and inductors, have been extensively used in the 1960's for amplification, frequency up- and down-conversion and oscillators at microwave frequencies [1], [3]. Parametric devices transfer the power from the pump frequency ( $\omega_p$ ) to the signal frequency ( $\omega_s$ ), as opposed to standard amplifiers which transfer power from the DC source to the signal frequency. Parametric amplifiers were commonly built using varactor diodes for microwave applications. Since parametric amplifiers are not based on semiconductor junction effects and resistors, they do not suffer from the Johnson, Shot and  $1/f$

noise, resulting in low noise amplifiers [3], [6]. With the introduction of MESFET's, parametric amplifiers were abandoned and to our knowledge, they are not used anymore in microwave applications. However, in recent years, the parametric amplification has seen a resurgence mostly in optical systems [7], [8].

This paper presents a micro-mechanical (MEMS) up-converter parametric-effect amplifier. The MEMS time-varying capacitor is composed of a thin low stress metallized silicon-nitride diaphragm and is pumped by a large signal voltage at a pump frequency  $f_p$ . This results in large changes in the capacitance and parametric amplification of an input signal at  $f_s$ . The design and analysis of this amplifier is presented in this paper, together with measurements of the transducer gain at 1.84 MHz.

The theory of parametric amplification is first presented and the capacitance values, impedances, and amplifier gain are calculated for various frequencies. It is seen that the capacitance of the MEMS varactor determines the input and output impedance of the parametric amplifier, and to obtain high amplification, the MEMS capacitor must be pumped such that the capacitance component at the pump frequency is at least 30 % of the DC capacitance. A detailed extraction technique based on I-V measurement of the MEMS capacitor under small signal and large signal conditions is then presented, and the capacitance, inductance, series resistance, and most important, the capacitance variation are determined. These values are used in the construction and measurement of a 200 kHz input/1.84 MHz output parametric up-converter amplifier.

The frequency of operation is controlled by the pump frequency (1.6 MHz in this case) and this can be increased up to 10-20 MHz by reducing the size of the MEMS diaphragm. The MEMS varactor is built using silicon micromachining techniques, but can also be integrated on ceramic, quartz, etc.. substrates, resulting in a 10 kHz - 10 MHz amplifier on non-semiconductor based substrates. This may be excellent for high temperature sensors, nuclear sensors, or piezoelectric sensors for sonar applications.

## II. REVIEW OF PARAMETRIC AMPLIFICATION

### A. Manley-Rowe Equations

A pair of equations governing the power balance of a one-port passive and lossless device with an arbitrary nonlinear characteristic has been derived by Manley and Rowe [11]. The device is simultaneously fed by a signal source at frequency  $f_s$  and a pump source at frequency  $f_p$ . Active power  $P_{n,m}$  at the combination frequency  $f_{n,m}$  is positive if it is fed to the nonlinear reactance, and negative if it is delivered by the nonlinear reactance. The following pair of equations then hold:

$$\sum_{n=0}^{\infty} \sum_{m=-\infty}^{\infty} \frac{nP_{n,m}}{nf_s + mf_p} = 0 \quad (1)$$

$$\sum_{m=0}^{\infty} \sum_{n=-\infty}^{\infty} \frac{mP_{n,m}}{nf_s + mf_p} = 0 \quad (2)$$

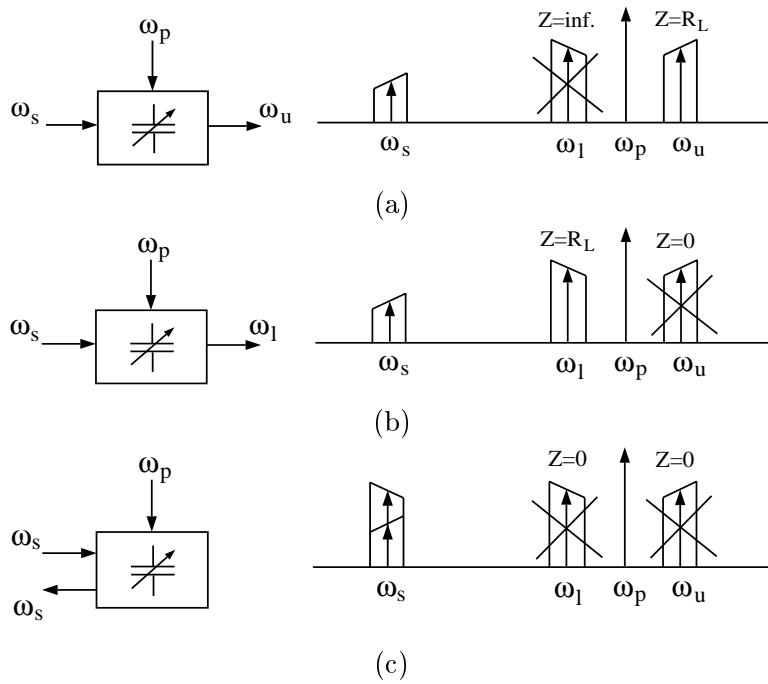


Figure 1: Amplitude spectra for parametric amplifier three-frequency operation; noninverting up-converter (a), inverting up-converter (b) and inverting negative resistance amplifier (c).

The general result (1)-(2) can be simplified by allowing only a single combination frequency  $f_2$  besides the signal frequency  $f_s$  and the pump frequency  $f_p$ . All other combination frequencies are filtered. Two choices for  $f_2$  are:

$f_2 = f_u = f_p + f_s =$  *upper sideband frequency*, and since  $f_2$  and  $f_s$  vary in the same direction, this is called the frequency-noninverting case, and results in (Fig. 1):

$$P_{p+s} = -\frac{f_p + f_s}{f_s} P_s \quad (3)$$

Also,

$f_2 = f_l = f_p - f_s =$  *lower sideband frequency*, and since  $f_2$  and  $f_s$  vary in opposite directions, this choice is called the frequency-inverting case (Fig. 1).

$$P_{p-s} = -\frac{f_p - f_s}{f_s} P_s \quad (4)$$

Notice that the power gain is equal to the ratio of the output to input frequencies, and this can be as much as 10:1 or even 50:1 depending on the choice of  $f_s$  and  $f_p$ . The output power is negative due to the definition of powers in the Manley/Rowe equations.

## B. Gain, Bandwidth and Stability of Parametric Amplifier/Up-Converter

The parametric amplifier is based on a time-varying capacitance  $C(t)$  which is pumped at  $\omega_p$  and is defined by [13]:

$$C(t) = C_0(1 + 2\gamma_1 \cos(\omega_p t) + 2\gamma_2 \cos(2\omega_p t) + 2\gamma_3 \cos(3\omega_p t) + \dots) \quad (5)$$

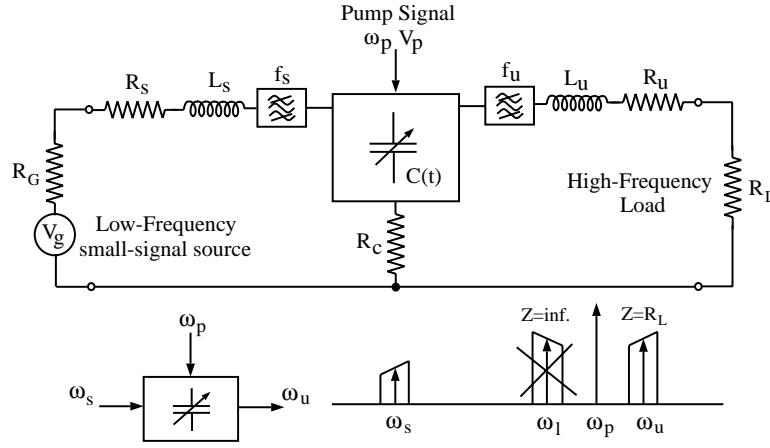


Figure 2: Equivalent model and amplitude spectra for a generic parametric up-converter. The time-varying capacitance is pumped at  $\omega_p$ , the input signal frequency is  $\omega_s$  and the up-converted frequency is  $\omega_u = \omega_p + \omega_s$ . Since we consider ideal input and output filters, all other sidebands are terminated with an open-circuit.

where  $C_0$  is the DC capacitance of the MEMS amplifier. The time-varying capacitor is terminated in an ideal filter such that signal voltages at only the three frequencies  $f_p$ ,  $f_u = f_p + f_s$ , and  $f_l = f_p - f_s$  are allowed to exist. The non-inverting case is of particular importance since it can be proven [12] that the device is stable and yields maximum gain with a matched source and load impedance. Under matched conditions, the power gain is equal to the ratio of output to input frequencies as determined by the equations above.

The transducer gain corresponding to the equivalent circuit presented in Fig. 2 is defined as the ratio of the output power in a load, ( $|I_L|^2 R_L$ ), to the available input power from the source, ( $\frac{|V_G|^2}{4R_G}$ ), and is calculated by [13] as:

$$G_t = \frac{4R_G R_L |I_L|^2}{|V_G|^2} = \frac{4R_G R_L |Z_{21}|^2}{|(Z_{11} + Z_{T1})(Z_{22} + Z_{T2}) - Z_{12} Z_{21}|^2} \quad (6)$$

with  $Z_{ij}$  being the two-port  $Z$ -parameters of the time-varying capacitor  $C(t)$ ,  $Z_{T1}$  is the total external circuit impedance at  $f_s$  ( $Z_{T1} = X_s + R_s + R_G + R_c$ ), and  $Z_{T2}$  is the total external circuit impedance at  $f_u$  ( $Z_{T2} = X_u + R_u + R_L + R_c$ ).

Neglecting the losses of the matching inductances  $L_s$  and  $L_u$  ( $R_s = R_u = 0$ ), the maximum transducer gain and bandwidth are obtained under the matched conditions defined as follows:

$$R_G = R_L = R_c \sqrt{1 + \frac{\gamma^2}{\omega_s \omega_u C^2 R_c^2}} \quad (7)$$

$$X_s = \frac{1}{\omega_s C} \quad (8)$$

$$X_u = \frac{1}{\omega_u C} \quad (9)$$

with

$$\gamma = \gamma_1(1 + \gamma_1^2)(1 - \gamma_2) \quad (10)$$

$$C = C_0(1 - 2\gamma_1^2) \quad (11)$$

and is given by:

$$G_t = \frac{f_p + f_s}{f_s} \frac{x}{[1 + \sqrt{1 + x}]^2} \quad (12)$$

where  $x = (f_p + f_s)/f_s(\gamma Q)^2$ , and  $Q$  is the effective quality factor of the time-varying capacitor ( $Q = 1/\omega_s C R_c$ ).

For the case of a lossless time-varying capacitor ( $Q \rightarrow \infty$ , i.e.  $R_c = 0$ ), the transducer gain tends to the ratio  $(f_p + f_s)/f_s$  which is in accordance with the power gain defined from the Manley and Rowe relationships (3) for the non-inverting case. The input and output impedances become:

$$R_G = R_L = \frac{\gamma}{\sqrt{\omega_s \omega_u} C} \quad (13)$$

Note that the parametric amplifier does not result in an impedance transformation between the input and output ports, and  $R_G = R_L$ , even if the input and output frequencies are separated by a ratio of 10:1. Also,  $R_G$  is dependent on  $1/C$ , and a low impedance parametric amplifiers require a large value MEMS capacitor.

Under the matching conditions defined above, the maximum 3-dB gain bandwidth of the up-converter is independent of the capacitance and is given by:

$$BW = \gamma \sqrt{2f_u f_s} \quad (14)$$

The parametric amplifier can be designed to fit any transducer impedance by choosing the DC capacitance ( $C_0$ ) and the capacitance modulation ratio ( $\gamma_i = C_i/C_0$ ). Table I shows the calculated parameters for parametric amplifiers with varying  $C_0$ ,  $\gamma$  and  $f_s$ . It is seen that a large gain and bandwidth can be obtained using parametric amplifiers at 100-1000 kHz, with an impedance anywhere from 50 k $\Omega$  to 50  $\Omega$ .

$C_0$ [pF]	$f_s$ [kHz]	$f_p$ [MHz]	$f_u$ [MHz]	$\gamma_1$	$R_G = R_L$ [ $\Omega$ ]	Gain [dB]	BW [kHz]
10	100	10	10.1	0.2	3400	20	284
10	100	10	10.1	0.4	9725	20	568
10	300	10	10.3	0.2	1900	15.3	497
10	300	10	10.3	0.4	5560	15.3	994
10	1000	10	11	0.2	1030	10.3	938
10	1000	10	11	0.4	2950	10.4	1876
100	100	10	10.1	0.2	340	19.8	284
100	100	10	10.1	0.4	973	20	568
100	300	10	10.3	0.2	195	14.9	497
100	300	10	10.3	0.4	556	15.2	994
100	1000	10	11	0.2	104	9.6	938
100	1000	10	11	0.4	295	10.1	1876
500	100	10	10.1	0.2	69	18.8	284
500	100	10	10.1	0.4	195	19.6	570
500	300	10	10.3	0.2	40	13.1	500
500	300	10	10.3	0.4	112	14.6	994
500	1000	10	11	0.2	22.9	6.4	938
500	1000	10	11	0.4	60	8.9	1876

Table I. Simulations for the parametric amplifier with different frequencies and capacitor values.

### III. MEMS VARACTOR FABRICATION PROCESS

The central component of the MEMS parametric up-converter is a time-varying capacitor which consists of a thin diaphragm of metallized silicon nitride (top electrode) suspended above a heavily doped silicon bulk (bottom electrode). In order to decrease the magnitude of the pump signal voltage, the structure is made resonant at the pump frequency, the gap between the thin metallized silicon nitride diaphragm and the heavily doped silicon bulk is minimized ( $0.75 \mu\text{m}$ ) and the backside air loading (air between electrodes) is evacuated for reducing damping effects.

The main fabrication steps are shown in Fig. 3. A n-type (100) silicon wafer is heavily doped to achieve good conductivity at the wafer surface (defining the bottom electrode) and a  $0.75 \mu\text{m}$  oxide layer is grown with a wet oxidation process. This thin oxide layer is used as a sacrificial layer in the process. The oxide layer is patterned in order to define the diameter of the resonant silicon nitride membrane and etched using buffered hydrofluoric acid (BHF). A  $6000 \text{ \AA}$  layer of LPCVD nitride is then deposited. The residual stress of the nitride can be varied by changing the proportion of silane to ammonia during the deposition process [2]. The residual stress obtained is around 170 MPa. A pattern of holes is then transferred to the wafer with an electron beam lithography process. The nitride is plasma etched and the sacrificial oxide is removed with BHF to release the silicon nitride diaphragm. A low temperature oxide (LTO) layer is deposited and patterned on the released membrane, vacuum sealing the holes. Finally, aluminum is sputtered

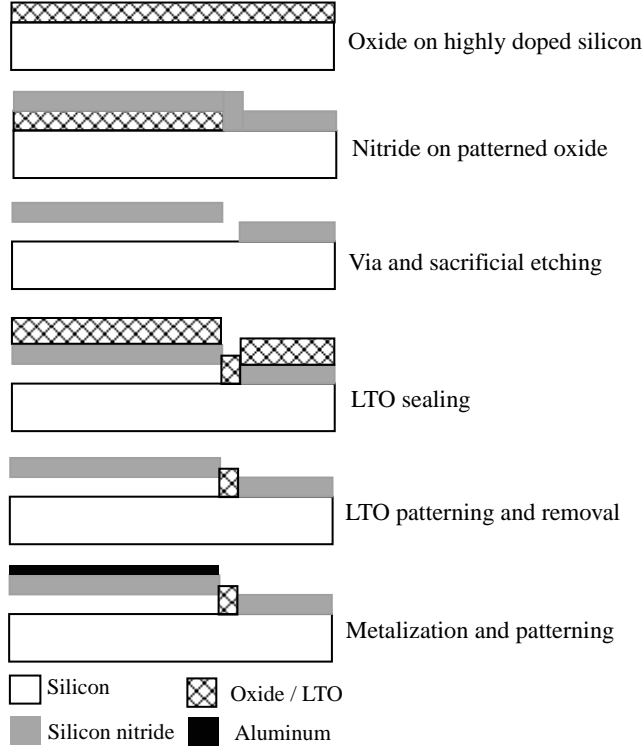


Figure 3: Fabrication steps of the MEMS capacitor.

and patterned to act as the top electrode. The same aluminum deposition also defines bonding contacts to the bottom electrode through a lithographically defined trench in the silicon bulk.

The fabricated capacitor is composed of 4500 orthogonally shaped silicon nitride membranes with  $0.6 \mu\text{m}$  thickness ( $t$ ), a residual stress ( $\sigma$ ) of 170 MPa,  $0.75 \mu\text{m}$  gap ( $g$ ) and  $50 \mu\text{m}$  radius ( $R$ ). The total device area is around  $1 \text{ cm}^2$ . The cross section and top view of the built MEMS capacitor are presented in Fig. 4.

#### IV. CHARACTERIZATION OF THE MEMS CAPACITOR

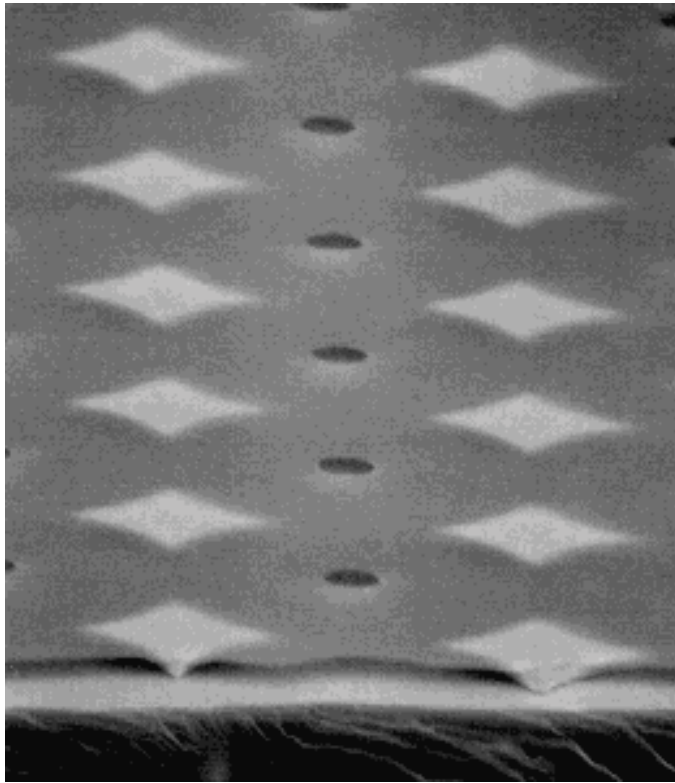
The MEMS capacitor is modeled as a suspended resonator, and the system is described by the second-order differential equation:

$$m \frac{d^2 x}{dt^2} + b \frac{dx}{dt} + kx = F_{electro}(x, t) \quad (15)$$

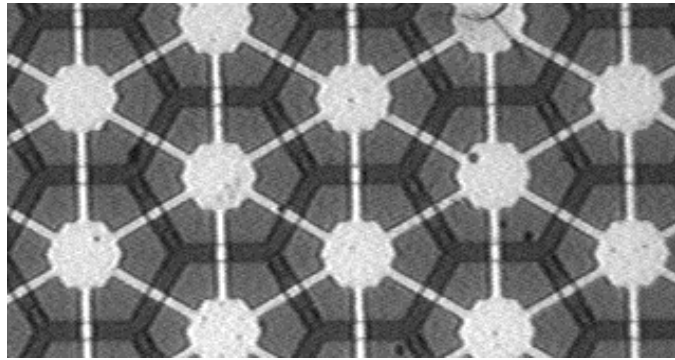
where  $m$  is the mass of the movable structure in kg,  $b$  the damping coefficient in N.s/m,  $k$  the spring constant in N/m and  $F_{electro}$  is the electrostatic applied external force in N. For a circular diaphragm, the coefficients are:

$$m = \rho \pi R^2 t_m \quad (16)$$

$$k = \frac{16 \pi E t_m^3}{3 R^2 (1 - \nu^2)} + 4 \pi \sigma t \quad (17)$$



(a)



(b)

Figure 4: (a) SEM cross section and (b) top view photograph of the built MEMS capacitor.

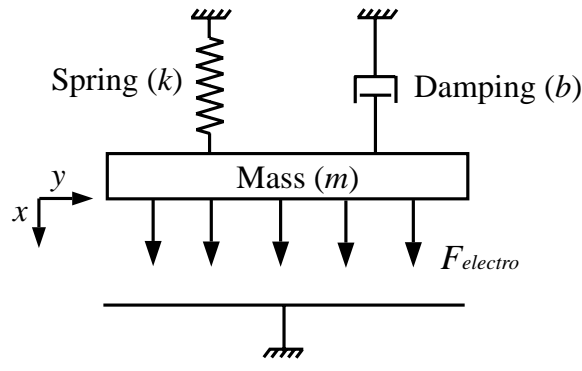


Figure 5: Simplified mechanical model of the MEMS time-varying capacitor.

and  $\rho$  is the density of the diaphragm material,  $R$  the radius,  $t_m$  the thickness of the diaphragm,  $E$  is the Young's Modulus in Pa of the beam material,  $\sigma$  is the residual stress in the beam and  $\nu$  the Poisson's ratio.

The damping coefficient ( $b$ ) expresses the energy dissipation in the system by airflow force, squeeze force, internal friction and support loss, and is related to the mechanical quality factor ( $Q$ ) of the mechanical structure as:

$$b = \sqrt{\frac{km}{Q^2}} \quad (18)$$

### A. Static Analysis

The external electrostatic force ( $F_e$ ) is induced by the source voltage  $V$ , and is applied between the two electrodes of the MEMS capacitor.

$$F_e = \frac{\epsilon_0 A}{2(g-x)^2} V^2 \quad (19)$$

where  $x$  is the displacement of the membrane from the zero-voltage position,  $A$  is the area of the membrane and  $g$  is the gap between the two electrodes.

In the electrostatic case, equation (15) is simplified as:

$$k(g-x) = \frac{1}{2} \frac{\epsilon_0 A V^2}{x^2} \quad (20)$$

and

$$V = \sqrt{\frac{2k}{\epsilon_0 A} x^2 (g-x)} \quad (21)$$

Fig. 6 shows the measured capacitance variation of the MEMS capacitor (described in section III) versus dc bias conditions. At  $\frac{2}{3}g$ , the increase in the electrostatic force is much greater than the increase in the restoring force. Substituting  $x = \frac{2}{3}g$  into (21), the "pull-down" or "collapse" voltage is:

$$V_p = V\left(\frac{2}{3}g\right) = \sqrt{\frac{8k}{27\epsilon_0 A} g^3} \quad (22)$$

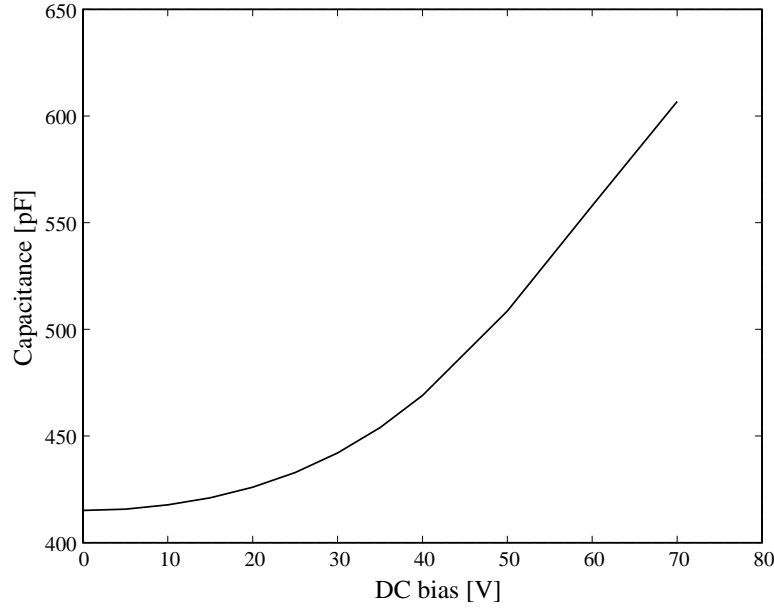


Figure 6: Capacitance variation versus dc voltage.

The calculated collapse potential for the measured structure is 85 V. It is important to work under this limit to avoid the collapse of the capacitive membrane.

### B. Small-Signal Analysis

In the linear case, we assume that the displacements ( $x$ ) are small compared to the physical gap dimension of the structure. The capacitor is driven by a small ac voltage  $v_{ac}$  superimposed on the dc bias ( $V_{dc}$ ), and this induces a small ac displacement  $x$ . The dynamics of the resonator are approximately determined by the second-order ordinary differential equation:

$$m\ddot{x} + b\dot{x} + kx = f_e \quad (23)$$

where  $f_e$  is the small signal ac electrostatic force expressed by:

$$f_e = \left( \frac{\partial F_e}{\partial V_{dc}} \right) v_{ac} = \frac{C_0 V_{dc} v_{ac}}{g_0} \quad (24)$$

and  $C_0 = \epsilon_0 A / g_0$ . This results in:

$$\hat{x} = \left( \frac{C_0 V_{dc}}{g_0 m \omega_0^2} \right) \frac{v_{ac}}{1 - \frac{\omega^2}{\omega_0^2} + j \frac{\omega b}{m \omega_0^2}} \quad (25)$$

where  $\hat{x}$  is the phasor representation of the displacement, and  $\omega_0 = \sqrt{k/m}$  is the resonant frequency.

By analogy, the spring-mass-damping system in Fig. 5 can be represented by the equivalent small-signal electrical circuit in Fig. 7. In this model,  $R_c$  represents the conductor losses in the capacitor plate and interconnect resistance. The capacitor current is:

$$i \approx C_0 \dot{v}_{ac} + V_{dc} \dot{C}_0 \approx C_0 \dot{v}_{ac} + \left( \frac{V_{dc} C_0}{g_0} \right) \dot{x} \quad (26)$$

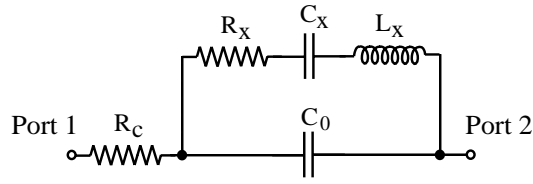


Figure 7: Small-signal electrical model of the MEMS capacitor.

and in phasor form:

$$\hat{i} \approx j\omega C_0 v_{ac} + j\omega \left( \frac{V_{dc} C_0}{g_0} \right) \hat{x} \quad (27)$$

$v_{ac}$  is the ac small-signal applied across the capacitance  $C_0$ . From (25), the current is:  $\hat{i} = (Y_0 + Y_x)v_{ac}$ , with  $Y_0 = j\omega C_0$  and

$$Y_x = \frac{j\omega C_x}{1 - \frac{\omega^2}{\omega_0^2} + j\omega C_x R_x} \quad (28)$$

By comparing equations (25) and (28), the equivalent electrical elements  $C_x$ ,  $L_x$  and  $R_x$  can be related to the mechanical quantities as:

$$C_x = \frac{C_0^2 V_{dc}^2}{k d_0^2} \quad (29)$$

$$L_x = \frac{m g_0^2}{C_0^2 V_{dc}^2} \quad (30)$$

$$R_x = \frac{b g_0^2}{C_0^2 V_{dc}^2} \quad (31)$$

The quality factor is given by:

$$Q = \frac{\omega_0 L_x}{R_x} \quad (32)$$

The device is first measured using an S-parameter set-up and a low frequency network analyzer (Fig. 8). The  $50 \Omega$ -based two-port measurements are used to obtain the Y-parameters of the capacitor [5], and the equivalent model is extracted as described below.

Far away above the mechanical resonant frequency, the suspended membrane does not vibrate with the ac small-signal voltage  $v_{ac}$ . The equivalent admittance ( $Y_x$ ) of the LCR series circuit therefore tends to zero (Fig. 7). Under high-frequency conditions, the inverse of the admittance  $Y_{12}$  can be simplified as:

$$Y_{12}^{-1} = -\frac{j\omega C_0 R_c + 1}{j\omega C_0} \approx -R_c \quad (33)$$

The quality factor  $Q$  of the MEMS capacitor is obtained by the 3-dB peak width method and the phase condition:

$$Q = \frac{\omega_0}{2} \left. \frac{\partial \phi_{12}}{\partial \omega} \right|_{\omega_0} \quad (34)$$

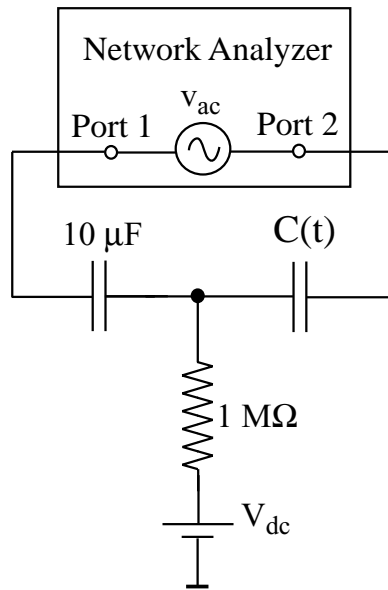


Figure 8: Set-up measurement for the extraction of the MEMS capacitor small-signal electrical model.

The resonant frequency  $\omega_0$  of the plate capacitor is extracted at  $\frac{\partial Q}{\partial \omega} = 0$ .  $R_x$  is obtained directly from the following subtraction:

$$R_x = \max(\text{Re}(-1/Y_{12})) - R_c \quad (35)$$

The equivalent inductance  $L_x$  and capacitance  $C_x$  can be extracted by  $QR_x/\omega_0$  and  $1/(\omega_0^2 L_x)$ , respectively. At very low frequency (few kHz) the equivalent capacitance  $C_0$  is obtained as  $\text{Im}(-Y_{12}) - C_x$ .

Fig. 9 and Fig. 10 represent the measured  $\text{Re}(1/Y_{12})$  and  $\text{Im}(Y_{12})/\omega$  for the fabricated MEMS capacitor described in Section III. The extracted values obtained for  $R_c$ ,  $C_0$ ,  $C_x$ ,  $L_x$  and  $R_x$  are respectively,  $55 \Omega$ ,  $500 \text{ pF}$ ,  $32.5 \text{ pF}$ ,  $0.29 \text{ mH}$  and  $166 \Omega$ . The resonant frequency of the MEMS plate capacitor is  $1.64 \text{ MHz}$  and has a quality factor of 18.

### C. Large-Signal Analysis

If a large pump signal voltage is applied, the displacements of the suspended diaphragm become non-negligible compared to the gap ( $g_0$ ), and a time-varying capacitance  $C(t)$  is generated:

$$C(t) = C_0(1 + 2\gamma_1 \cos(\omega_p t + \varphi_1) + 2\gamma_2 \cos(2\omega_p t + \varphi_2) + 2\gamma_3 \cos(3\omega_p t + \varphi_3) + \dots) \quad (36)$$

with  $\varphi_n \rightarrow 0$  when  $R_c \rightarrow 0$ . The extraction of the Fourier Series coefficients is an essential step for designing a parametric amplifier/up-converter. Two cases are considered: Case 1 with  $R_c \rightarrow 0$  and Case 2, with  $R_c$  being substantial compared to  $1/(j\omega C_0)$ .

Case 1: If  $R_c$  is small such that  $j\omega R_c C_0 \ll 1$ , the coefficients  $\gamma_i$  can be extracted directly from the current spectrum magnitude (measured with a spectrum analyzer). The current through the time-varying MEMS capacitor with a voltage signal  $v(t) = V_{dc} + v_{ac} \cos(\omega_p t)$  can be expressed as:

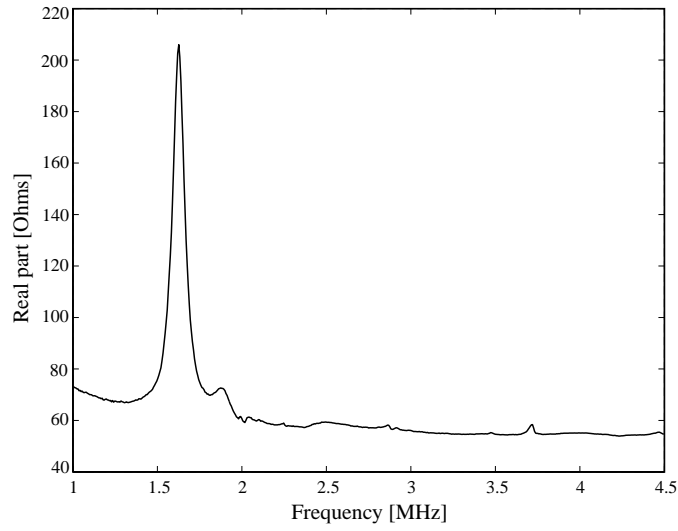


Figure 9: Real part of  $1/Y_{12}$  versus frequency.

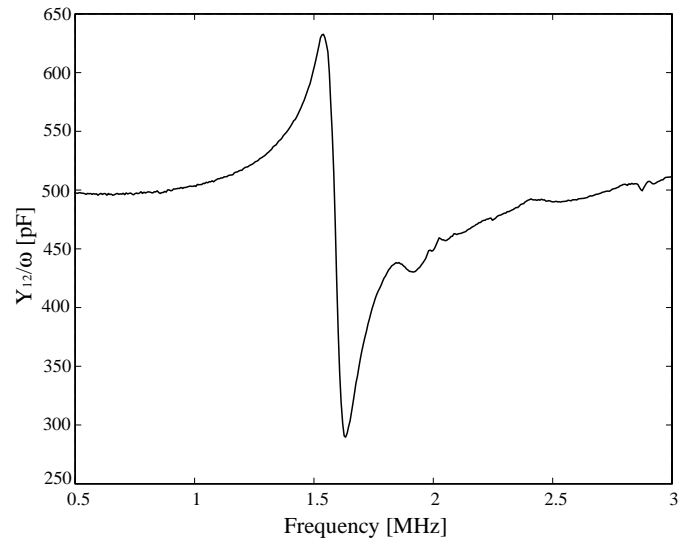


Figure 10: Equivalent capacitance ( $Y_{12}/\omega$ ) around the resonant frequency.

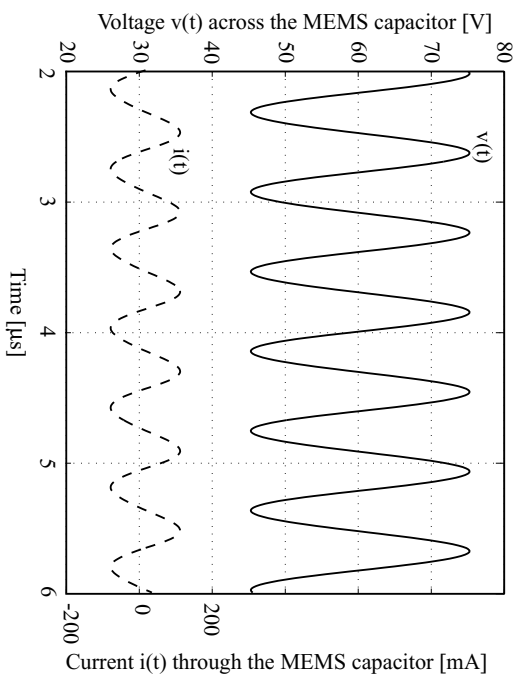


Figure 11: Measured voltage  $v(t)$  and current  $i(t)$  flowing through the time-varying capacitor  $C(t)$ .

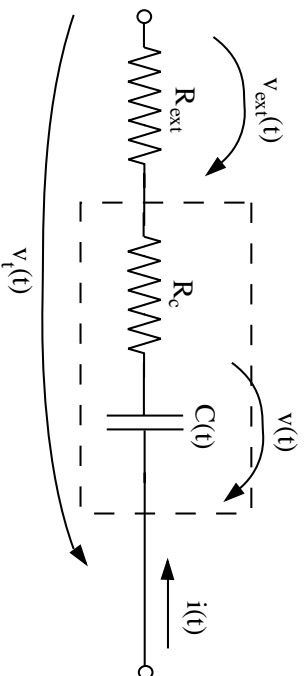


Figure 12: Scheme for extraction of  $C(t)$  expression.

$$i(t) = \frac{dq(t)}{dt} = \frac{d(C(t)v(t))}{dt} \quad (37)$$

and the following expressions for the three first harmonics are obtained:

$$I(\omega_p) = v_{ac}j\omega_p\pi C_0(1 + \gamma_2) + V_{dc}j\omega_p^2\pi\gamma_1 C_0 \quad (38)$$

$$I(2\omega_p) = v_{ac}j2\omega_p\pi C_0(\gamma_1 + \gamma_3) + V_{dc}j2\omega_p^2\pi\gamma_2 C_0 \quad (39)$$

$$I(3\omega_p) = v_{ac}j3\omega_p\pi C_0\gamma_2 + V_{dc}j3\omega_p^2\pi\gamma_3 C_0 \quad (40)$$

Case 2: If  $R_c$  is not negligible, the current *phase* also needs to be measured to determine the  $\gamma_i$  and  $\varphi_i$  coefficients. The phase information can be obtained by the measurement of the voltage  $v(t)$  and the current  $i(t)$  (Fig. 11). The current  $i(t)$  is easily obtained by measuring the voltage across an external resistor  $R_{ext}$  connected in series with the time-varying capacitor (Fig. 12).

Knowing the value of the parasitic resistor  $R_c$  (extracted from the small-signal analysis), the voltage across  $C(t)$  is given by  $v(t) = v_t(t) - i(t)(R_{ext} + R_c)$ , and the time-varying capacitance can be obtained numerically from:

$$C(t) = \frac{\int i(t)dt + C_0 V_{dc}}{v(t)} \quad (41)$$

Since  $C(t)$  is a periodic function, it can be decomposed as:

$$C(t) = C_0(1 + \sum_n 2\gamma_n \cos(\omega_n t + \varphi_n)) \quad (42)$$

or

$$C(t) = C_0(1 + \sum_n 2\gamma_{nc} \cos(\omega_n t) + 2\gamma_{ns} \sin(\omega_n t)) \quad (43)$$

Then, from the calculated numerical function  $C(t)$  (36), the coefficients  $\gamma_n$  and  $\varphi_n$  are extracted as:

$$\gamma_{nc} = \frac{1}{2C_0} \frac{2}{T} \sum_{t=0}^T (C(t) \cdot \cos(\omega_n t)) \quad (44)$$

$$\gamma_{ns} = \frac{1}{2C_0} \frac{2}{T} \sum_{t=0}^T (C(t) \cdot \sin(\omega_n t)) \quad (45)$$

with

$$C_0 = \frac{1}{T} \sum_{t=0}^T C(t) \quad (46)$$

From (44)-(45) the coefficients  $\gamma_n$  and  $\varphi_n$  in (42) are given by:

$$\varphi_n = -\arctan\left(\frac{\gamma_{nc}}{\gamma_{ns}}\right) \quad (47)$$

$$\gamma_n = \sqrt{\gamma_{nc}^2 + \gamma_{ns}^2} \quad (48)$$

Fig. 13 shows the measured harmonics level at the 50  $\Omega$  input of the spectrum analyzer for the MEMS time-varying capacitance pumped with an ac large signal  $v(t) = 60 + 15 \cos(\omega_p t)$  with  $\omega_p = 1.64$  MHz. Due to the high value of  $R_c$  (55  $\Omega$ ) as compared to the equivalent impedance ( $1/(j\omega C(\omega)) = 400 \Omega$ ) of the MEMS capacitor, the second extraction method is used. The calculated  $C(t)$  is presented in Fig. 14, and the coefficients  $\gamma_n$  and  $\varphi_n$  are found to be:  $\gamma_1 = 0.22$ ,  $\gamma_2 = 0.05$ ,  $\gamma_3 = 0.01$ , with  $\varphi_1 = 7^\circ$ ,  $\varphi_2 = 3^\circ$  and  $\varphi_3 = 0.85^\circ$ .

## V. MEMS PARAMETRIC UP-CONVERTER AMPLIFIER MEASUREMENTS

Fig. 2 shows the experimental set-up for the parametric amplifier. The input filter is a Chebyshev band-pass filter composed of three LC sections and is designed to pass 200 kHz. It

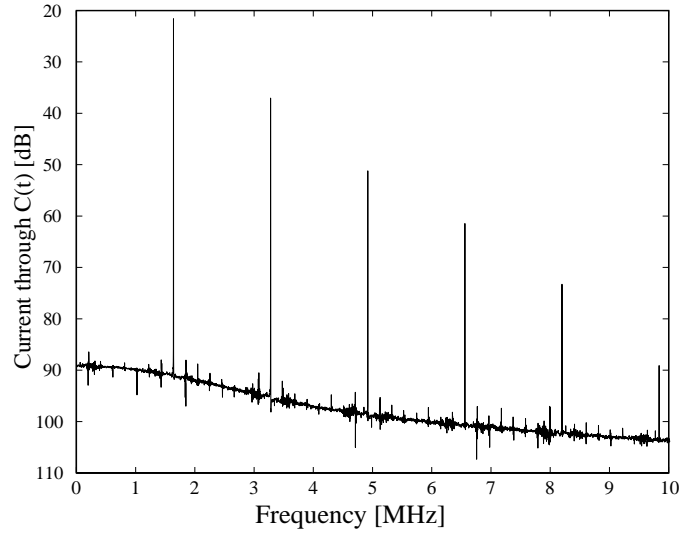


Figure 13: Spectrum of the current flowing through the MEMS time-varying capacitor for  $V_{dc} = 60$  V and  $v_{ac} = 15$  V.

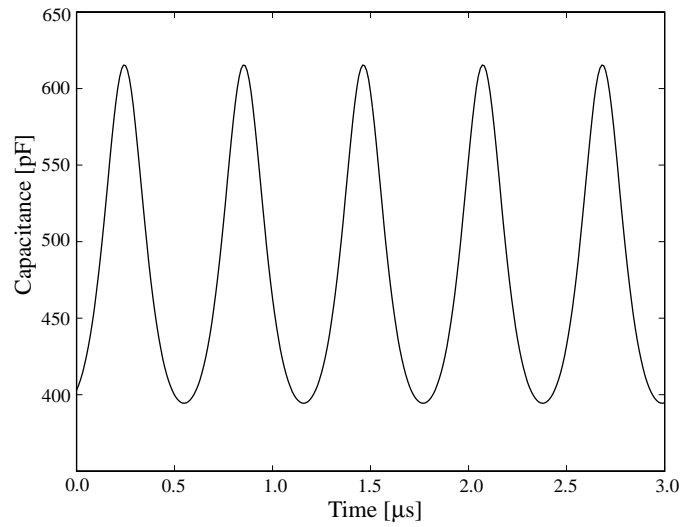


Figure 14: Time-varying capacitance versus time for  $V_{dc} = 60$  V and  $v_{ac} = 15$  V.

presents a bandwidth of 40 kHz and an insertion loss of 0.9 dB. The MEMS capacitor is pumped with a large ac signal at 1.64 MHz. In order to avoid power loss from the input signal at 200 kHz, and from the output signal at 1.84 MHz, into the pump signal generator, a Chebyshev band-pass filter composed of three LC sections is connected in series with the pump signal generator. This filter presents a bandwidth of 100 kHz and an insertion loss of 3.6 dB (not important for the gain measurements). The output filter is also a Chebyshev band-pass filter composed of three LC sections centered at 1.84 MHz with a bandwidth of 100 kHz. It attenuates greatly the image frequency at 1.44 MHz (-60 dB). The input and output filters are also designed to do an impedance transformation: from the generator ( $R_G = 140 \Omega$ ) to the equivalent input impedance of the time-varying capacitor  $C(t)$  ( $Z_{in}$  equal to  $R_{in} = \gamma^2 / (\omega_s \omega_u C^2 (R_c + R_L)) = 85 \Omega$  at the resonant frequency) for the input filter and from the equivalent output impedance of  $C(t)$  ( $Z_{out}$  equal to  $R_{out} = \gamma^2 / (\omega_s \omega_u C^2 (R_c + R_G)) = 85 \Omega$  at the resonant frequency) to the load ( $R_L = 140 \Omega$ ) for the output band-pass filter.

The measured and simulated transducer gain of the parametric up-converter versus the load resistance is shown in Fig. 15. After de-embedding the LC band-pass filters losses, we observe a good agreement between measurements and simulations. The gain was measured with an input signal from 0.1 to 0.5 V and was found to be the same. The measured optimum load of 135  $\Omega$  is in accordance with the value of 140  $\Omega$  calculated using (7). The bandwidth of operation was limited by the input filter to 40 kHz.

Fig.16 represents the measured transducer gain versus the coefficient  $\gamma_1$  which depends on the pump signal power. For these measurements, the matching networks and filters has been optimized for  $\gamma_1=0.22$ . As predicted by the simulations, the transducer gain of the MEMS parametric up-converter increases with  $\gamma_1$ . It is seen that one needs at least a  $\gamma_1 = 0.15$  in order to obtain any substantial gain from the circuit.

A pump signal magnitude  $v_{ac}$  of 15 V was used to achieve a coefficient  $\gamma_1$  equal to 0.22. This corresponds to a power of 560 mW delivered by the pump generator at 1.64 MHz. This high value of power is linked to the low mechanical quality factor  $Q$  of the measured structure ( $Q = 18$ ). In our case, the air loading (air in the front volume of the diaphragm) is the main factor which contributes to the global damping term of the mechanical structure. Wang et al. [4] have shown that it is possible to achieve quality factors of 10,000 under vacuum condition at 1-2 MHz. For this high value of  $Q$ , a coefficient  $\gamma_1$  of 0.22 can be reached with a pump signal  $v_{ac}$  of less than 1 V, resulting in a supply power at the pump frequency of less than 2 mW. Therefore, the optimization of the MEMS time-varying capacitor package is an important issue for reducing the power consumed by the novel MEMS parametric up-converter.

## VI. CONCLUSION

This paper presented the theory of operation of MEMS-based parametric amplifiers. The parametric amplifier is critically dependent on the capacitance variation, and an accurate technique is presented to determine the fourier coefficients of the MEMS capacitor under large signal

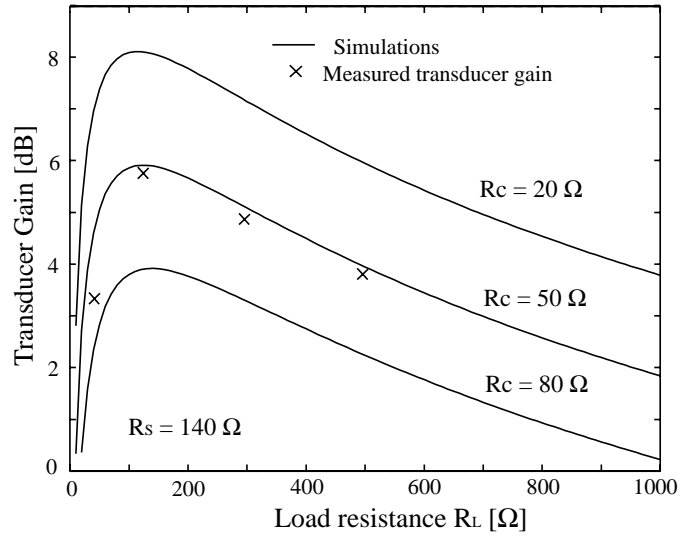


Figure 15: Parametric up-converter transducer gain versus the load resistance value.

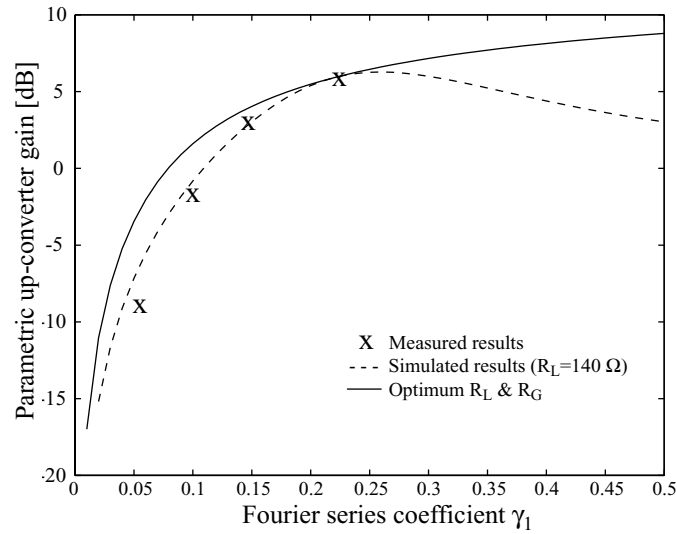


Figure 16: Parametric up-converter transducer gain versus  $\gamma_1$ .

conditions. A 200 kHz input/1.84 MHz output MEMS parametric amplifier with a measured gain of 6 dB was presented. In the future, it is possible to build higher frequency amplifiers (up to 1-5 MHz) using smaller area membranes. The advantage of this technique is that one can build amplifiers for sensors (thermal, pressure, acoustic, gravitational, chemical, etc.) without any CMOS electronics. Some applications include very high temperatures amplifiers (200-600 °C), high particle bombardment (nuclear applications) amplifiers, and very low  $1/f$  noise amplifiers.

### **Acknowledgements**

This work is supported by DARPA, under contract number DAAG55-98-10432. Edgar Martinez and Theo Kooj, contract monitors.

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