

## DEVELOPMENT OF A SUPERCONDUCTING INTEGRATED RECEIVER FOR APPLICATION IN IMAGING ARRAYS

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### *Introduction*

An imaging array for the sub-millimeter wavelength region based on a number of light weight and low power consuming devices such as the recently developed superconducting integrated receiver [1], looks very attractive, especially for space applications. Both reproducibility and reliability of the integrated receiver chips are very important to create an imaging array. We have demonstrated recently a noise temperature of 400 K DSB at 450 GHz using an on-chip Flux Flow Oscillator (FFO) as a local oscillator (LO). This was only 30% higher than the best performance obtained by using an external LO [2]. The main reason for the difference between the external and internal pump is lack of the LO power coupled from FFO to the SIS junction.

The experiment with frequency locking of the FFO at about 450 GHz to an external synthesized source of 10 GHz has been performed successfully. An averaged linewidth of about 200 kHz has been recorded [3]. The recent status of an integrated receiver study as well

as a number of specific problems associated with the development of an imaging array are reported.

### *Experimental Details*

The all-Nb superconducting integrated receiver on a single crystalline quartz chip of size  $4\text{ mm} \times 4\text{ mm} \times 0.2\text{ mm}$  contains: a planar double-dipole antenna, SIS mixer, FFO as a local oscillator and all necessary coupling circuitry. The photo of the chip receiver mounted in the mixer block by Al bonding ( $\varnothing 20\text{ }\mu\text{m}$ ) is presented in *Fig. 1*.

The transfer of the production process from IREE (Moscow, Russia) to SRON/FDL (Groningen, the Netherlands) has been realized successfully in a short time, which probably means that the integrated receiver chips are reasonably easy to produce world-wide. All the circuits have been redesigned for use of  $\text{SiO}_2$  insulation at SRON/FDL instead of  $\text{SiO}$  used at IREE. A yield of good devices as high as 80% has been achieved in a single batch (12 useful devices from 15 ones available). The data of production yield are presented in *Table 1*.

The optimization of the coupling circuitry between the SIS mixer and the FFO has resulted in considerable improvement of the pumping level of the SIS mixer [4]. The normalized RF voltage  $\alpha = eV_{\text{RF}}/hf$  as high as  $\alpha = 2$  has been achieved during preliminary dc-tests in dipstick. In the cryostat the antenna is matched better to the outcoming beam so part of LO power emitted those results in somewhat lower pump level.

The use of  $\mu$ -metal shield around the housing of the integrated receiver in the cryostat has resulted in the improvement of stability of the FFO in the noisy experimental environment. The recent improvements in the optics (anti-reflection coating on all quartz lenses of the mixer, thin mylar window of the cryostat) have resulted in significant decrease of the receiver DSB noise temperature lower than 150 K. The break-down of the noise for the experimental receiver is presented in *Table 2*.

It has been found that no significant difference occurs for the receiver driven with external or internal LO for both the pump level and the noise temperature of the receiver. The experimental data for the mixer as well as its hot/cold response at about 500 GHz are presented in *Fig. 2*. The plot of the receiver DSB noise temperature with respect to the LO

frequency is presented in *Fig. 3*.

The experimental antenna beam pattern of the integrated receiver is presented in *Fig. 4*. The difference in the width of H- and E-pattern is most probably caused by the double-dipole antenna shape which is designed to be used without back-reflector that means longer arms and higher directivity in the vertical plane.

The reported results of the integrated receiver look rather encouraging that allow us to start development of optical concept for integrated image array receiver.

### *Optical Configuration for an Image Array Receiver*

There are several possible optical configurations that could be used in an imaging array. Two different approaches of practical interest to an array are: A) a single dielectric lens antenna (either an elliptical or hyper hemispherical lens, *Figure 5a*) with an array of antennas (receivers) positioned in the focus of the lens, and B) an array of lenses each with its own receiver positioned in the focus of the lens ( fly's eye, *Figure 5b*). With the single lens concept an additional intervening optical system (mirrors and/or lenses) between the telescope and the dielectric lens antenna is needed to re-image the beams onto the sky, as for the fly's eye concept that depends on the dielectric lens type chosen.

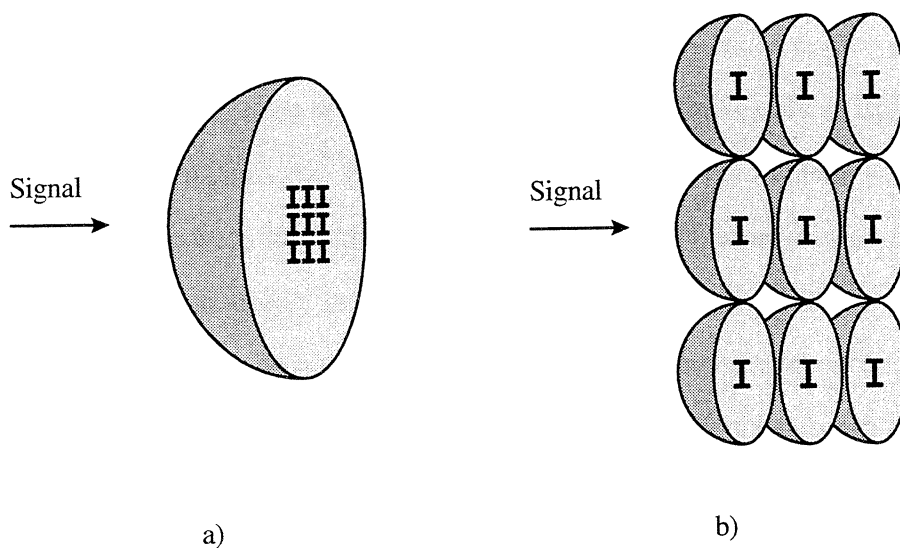


Figure 5. Two different array concepts for 9 pixel image receiver: a) A large single lens with

an array of receivers in its center, and b) an array of lenses, each only having a single element in its center.

Comparison of different optical schemes needs to be based on the performance, such as aperture efficiency, spillover, undersampling (packing density), sidelobe level and Gaussian beam coupling, of each system. Also practical considerations, such as the size of the whole array, the size of the intervening optics and whether there is a need for them, yield in the SIS-junction process, testability and selection of each receiver in the array, play an important role in selection of the best concept for the imaging array.

Both the single lens and the fly's eye concept offer considerable advantages ( $\oplus$ ) as well as disadvantages ( $\ominus$ ), which are more or less reciprocal of one another:

### **Single lens**

- $\oplus$  everything on a single chip, superconducting lines for DC and RF connections could be used (including phase-locking signals),
- $\oplus$  small size,
- $\oplus$  ease of mounting,
- $\ominus$  the image quality of the off-axis beams distorted due to aberrations,
- $\ominus$  crosstalk between individual elements,
  - mutual coupling (RF),
  - intermediate frequency coupling (IF),
  - magnetic coupling (DC),
  - reflections from the dielectric-air interface,
- $\ominus$  a large front lens needed,
- $\ominus$  yield in the SIS manufacturing process might not be high enough to give 9 (10) similar junctions on a single chip (plus FFOs),
- $\ominus$  broken pixel is non-replaceable since the receivers are manufactured on a single chip.

**"Fly's eye"**

- ⊕ all antennas are center elements  $\Rightarrow$  no aberrations,
- ⊕ possible to match directly to the telescope without any intervening optical system,
- ⊕ reduced crosstalk between individual elements,
- ⊕ each of the receivers can separately be tested and replaced, if needed,
- ⊕ size of the receiver chip much smaller than the lens size  $\Rightarrow$  room for connections,
- ⊖ large array size  $\Rightarrow$  large intervening optics if they are needed (sideband filtering, calibration etc.),
- ⊖ large number of individual lenses and receives  $\Rightarrow$  mounting laborious.

The problems associated in the single lens concept with the integrated receiver, such as crosstalk, packing density and yield in the SIS junctions process are obstacles that could be overcome with thorough study and testing with time. On the other hand, the "fly's eye" concept already offers solution to these problems, and in addition it offers high beam quality for all beams, since there are no aberrations. For the reasons presented we are in favour of the "fly's eye" configuration, but we have not yet completely ruled out the single lens concept either.

*Conclusion*

The recent results of the integrated receiver test:  $T_{RX} < 200$  K, reasonably low antenna sidelobes (-18...-20 dB), and quite high production yield (80%), allow us to start the development of a concept of the integrated image array for radio astronomical application.

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*References:*

[1] V. P. Koshelets, S. V. Shitov, L. V. Filippenko, A. M. Baryshev, H. Golstein, Th. de Graauw, W. Luinge, H. Schaeffer, H. van de Stadt. "First Implementation of a Superconducting Integrated Receiver at 450 GHz", *Appl. Phys. Lett.*, to be published in 1996.

[2] A. Baryshev, V. Koshelets, S. Shitov, L. Filippenko, Th. de Graauw, W. Luinge, H. van de Stadt, H. Schaeffer. "A Fully Superconducting 500 GHz Receiver with Integrated Flux-Flow Oscillator", *Extended Abstracts of ISEC'95*, pp. 419-421, 1995

[3] A. V. Shchukin, V. P. Koshelets, S. V. Shitov, L. V. Filippenko, and J. Mygind. "Linewidth of Flux-Flow Oscillators for Sub-mm Wave Receivers", *Extended Abstracts of ISEC'95*, pp. 416-418, 1995

[4] S. V. Shitov, V. P. Koshelets, L. V. Filippenko, A. M. Baryshev, H. Golstein, Th. de Graauw, W. Luinge, H. Schaeffer, H. van de Stadt. "Recent Development of Integrated Receiver for submm Applications", *Conference Digest of XX International Conference on Infrared and Millimeter Waves*, pp. 367-368, Orlando, Florida, December 11-15, 1995.

*Figures Capture:*

*Fig. 1* Photo of the chip receiver mounted on the hyper hemispherical lens by bonding.

*Fig. 2* Experimental data for the mixer pumped by FFO: autonomous and pumped IV-curves; hot/cold response at about 500 GHz.

*Fig. 3* Integrated receiver DSB noise temperature with respect to the FFO frequency.

*Fig. 4* Experimental antenna beam pattern of the integrated receiver.

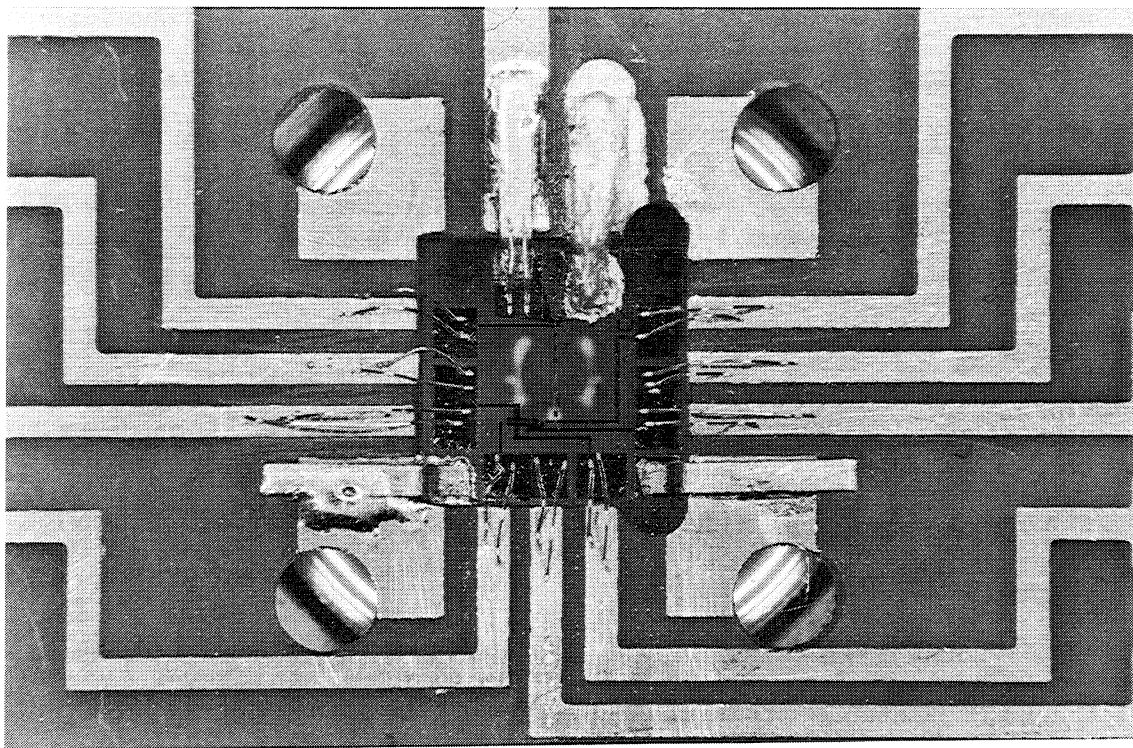


Fig. 1

Integrated Receiver with FFO as Local Oscillator

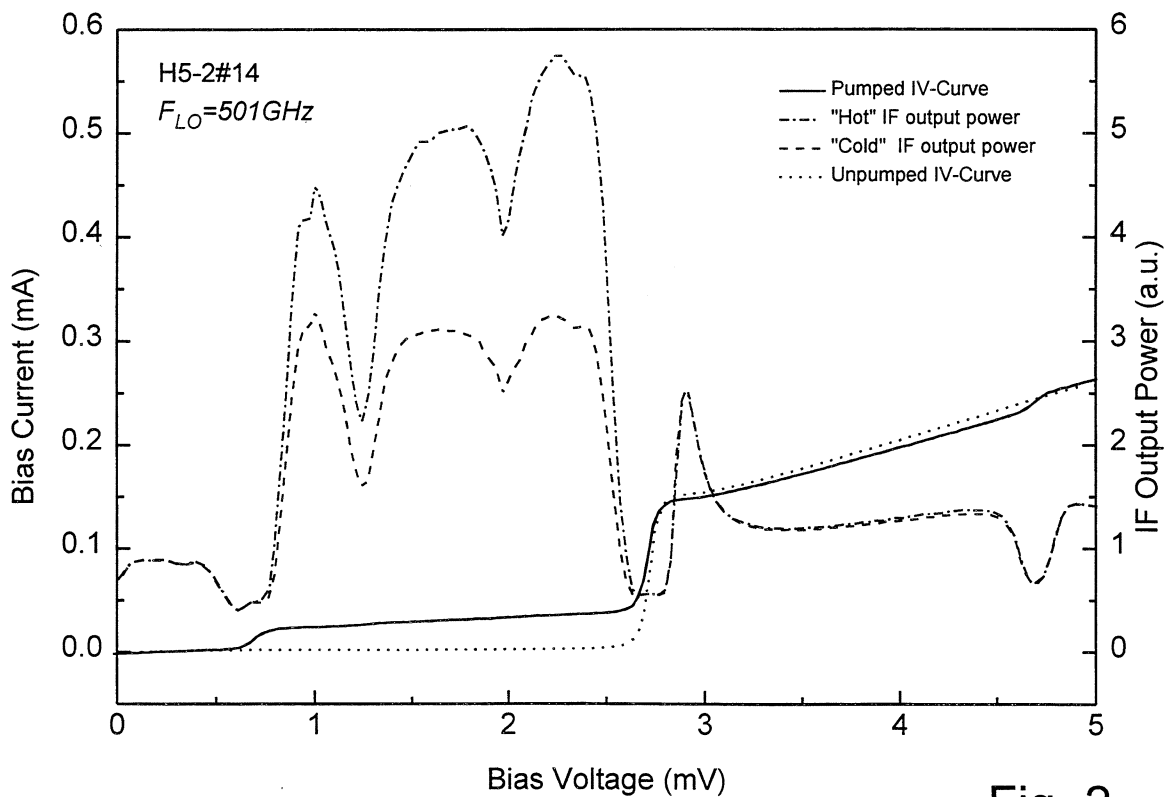


Fig. 2

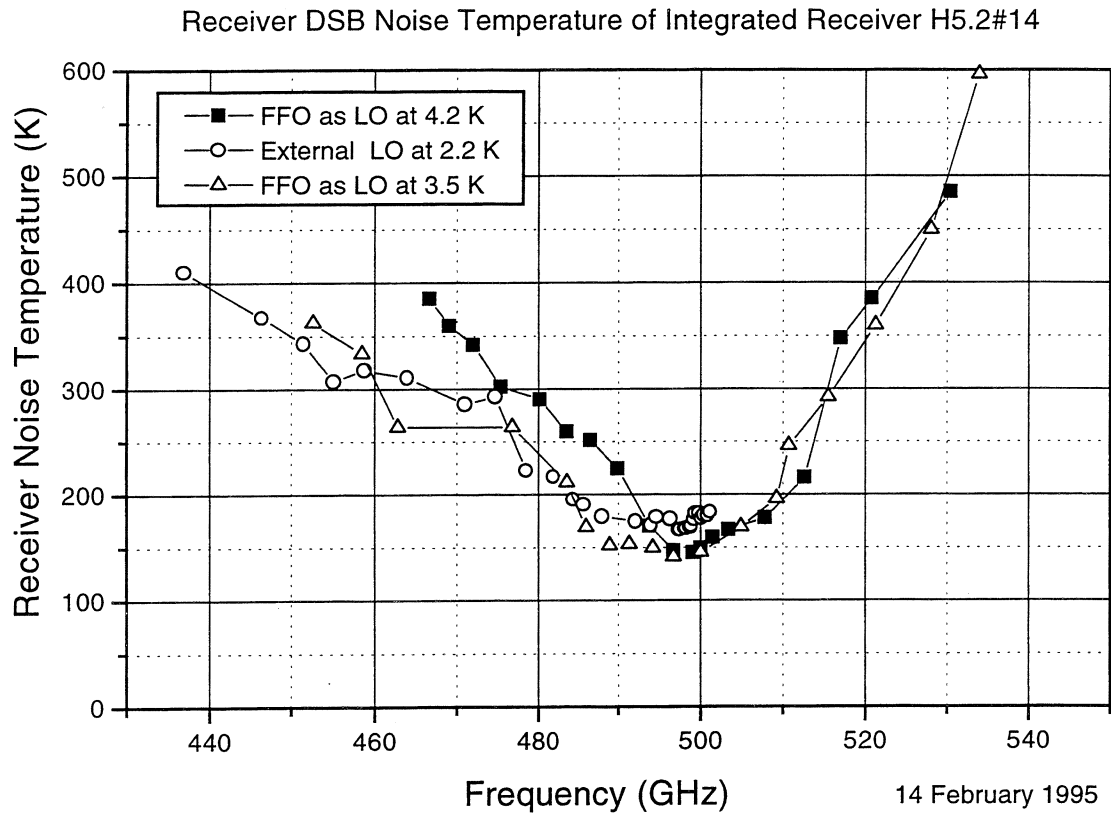


Fig. 3

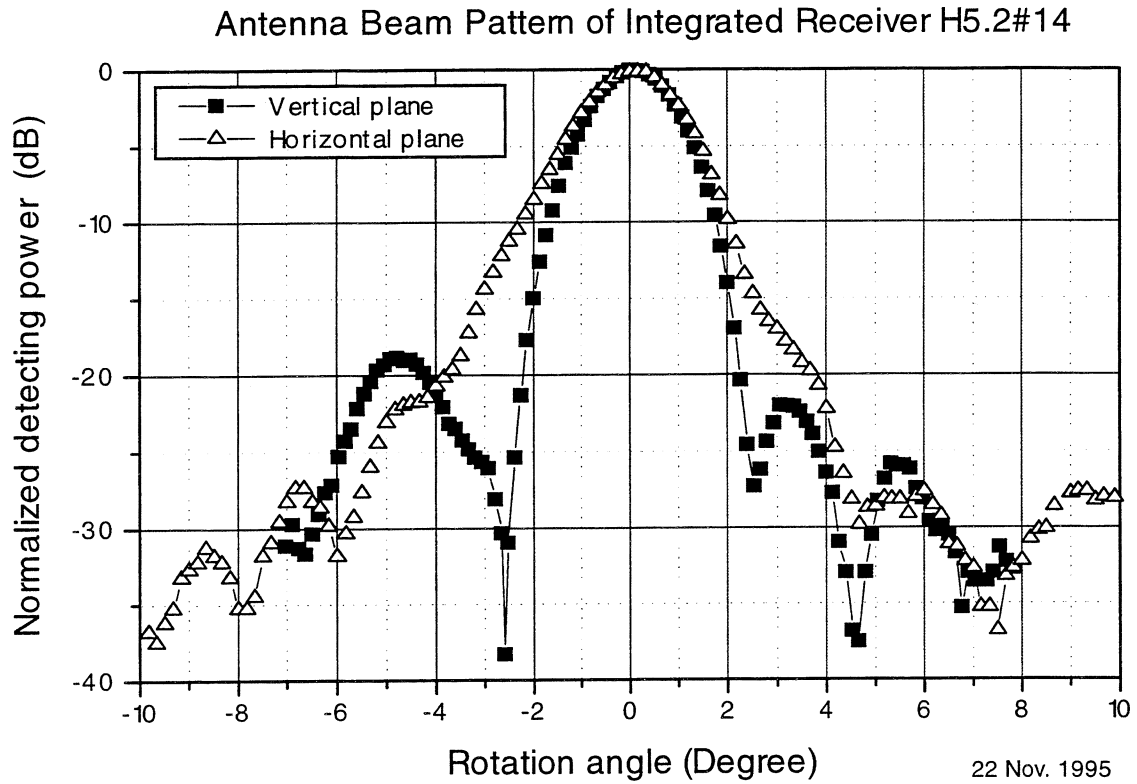


Fig. 4



Table 1

**Results of dc tests of Integrated Receiver Chips H5 (batch #2)**

#	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
SIS R <sub>n</sub> (Ω)		18	22.5	19.1	15.3	21	17.5	17.3	16.1	20	16	14.5	23.7	19.6	25	
SIS R <sub>r</sub> /R <sub>n</sub>		22	25	19	22	24	31	29	39	31	25	10	35	34	16	
SIS V <sub>g</sub> (μV)		2800	2800	2800	2800	2800	2830	2830	2825	2825	2780	2830	2835	2840	2800	
SIS δV <sub>g</sub> (μV)		150	200	200	150	200	120	120	150	140	160	160	130	120	120	
SIS CL 1st min/ (mA) 2nd min		50/ 100	60/ -	33/ 77	32/ 76	50/ 100	37/ 80	33/ 68	45/ 93	41/ 88	32/ 68	35/ 60	47/ 98	45/ 91	55/ -	
SIS area (μ <sup>2</sup> )		2.1	1.7	2.0	2.4	1.8	2.1	2.1	2.3	1.9	2.3		1.6	1.9	1.5	
FFO ΔI <sub>g</sub> (mA)		140	126	127	129	124	121	130	130	96	104	104	97	100	102	
FFO I <sub>r</sub> (mA)		3	3	3.3	3.5	3.3	3.1	3.4	3.3	2.7	2.7	2.7	2.5	3.2	5	
FFO I <sub>c</sub> (mA)		30	33	30	28	25	27	29	23	27	28	30	27	26	30	
FFO V <sub>g</sub> (μV)							2805		2825	2820	2830	2835	2835	2840	2810	
Flux Flow Steps		+	+	++	++	++	++	++	++	++	++	++	++	++	+	
SIS pumping, P.		+	+	+	++	++	++	+	++	++	++	++	++	++	++	
V of max P. (μV)		950	990	950	970	970	980	970	980	970	970	970	1100	1050	1050	
ΔI SIS at max P.		75	70	60	85	75	85	75	75	85	110	100	80	110	110	
minV/maxV(μV) where ΔI SIS > 20 μA		900/ 1060	950/ 1050	920/ 1020	920/ 1030	860/ 1120	860/ 1100		950/ 1100	950/ 1130			950/ 1130	955/ 1135		
# of comments	1	2	3	4	5	6										
Σ	-	+	+	+	+	+	++	+	++	++	++	++	+-	++	++	+

SIS V<sub>g</sub> (μV) Before modification of the measuring setup V<sub>g</sub> was measured by oscilloscope

SIS CL 1st min/2nd min (mA) Values of the SIS Control Line Current where 1st /and 2nd minimum of SIS critical current occur.

SIS area (μ<sup>2</sup>) Calculated from averaged R<sub>n</sub>S value

FFO ΔI<sub>g</sub> (mA) FFO “gap” current.

FFO I<sub>r</sub> (mA) FFO “return” current.

FFO V<sub>g</sub> (μV) Measured at 10 mA.

V of max P. (μV) FFO voltage where maximum pumping of SIS takes place.

ΔI SIS at max P. Maximum changing of the SIS current (μA) at 2 mV by FFO pumping.

Σ Summary Rating (possibility to use for some hf measurements: IntRec, Receiver with External LO, FTS)

Table 2

**Noise Figure Breakdown for Quasi-optical Integrated Receiver H5-2#7**

	T <sub>rx</sub> DSB (K)	T <sub>rx</sub> DSB corrected for beamsplitter 10 μm (K)	T <sub>rx</sub> DSB corrected for 10% sidelobes (K)	Mixer noise @ input (K)	IF noise @ input (K)	Optics efficiency (dB)	Antenna coupling (dB)	Mixer gain DSB (dB)	IF coupling R <sub>dvn</sub> =190Ohm (dB)	T <sub>IF</sub> (K)
Experimental	150±5	-	-	122±5 <sup>1</sup>	20±5	-0.7±0.3 <sup>2</sup>	-	-3.3±0.5	-	9±3
Theoretical	-	142±5	≈106	54 <sup>3</sup>	22	-0.45	-2.3±0.4	+2.05	-1.8	12

<sup>1</sup> Noise of the optics and sidelobes included  
<sup>2</sup> Part of the mixer gain  
<sup>3</sup> Quantum noise not included (26 K @ 500 GHz)