Research Projects & Sponsors

PI: Prof. Pinaki Mazumder, University of Michigan

AFOSR, ONR

Very Deep Submicron CMOS Design Tools: EMI Effects on Circuits, Thermal Modeling of 3-D Chips, Multivariate Statistical Timing, Dynamic Noise Modeling, SSN, DQM, FDQ, etc.

NSF, ONR, and DARPA

Nano Circuit And Device Simulation Tools: Q-Spice, VEDICS, QModel, QUECDOE

NSF, ONR, DARPA, Industries

Nano Circuit & System Design: Mesoscale & Nanoscale Devices Circuits and Architectures; Plasmonics

Memories: Testable Design Compilers Self-repair, Panorama

NSF, ARO

Layout synthesis: GA-based Distributed Layout Tools

Industries, ARO