

Mojtaba Mehrara

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RESEARCH INTERESTS

My research interests lie in the area of compilers and computer architecture. I am mainly interested in compiler and architecture techniques for improving programmability and usability of parallel systems.

EDUCATION

- | | | |
|--------------|---|---------------|
| 2007-present | University of Michigan PhD. Candidate - Computer Science and Engineering Advisor: Prof. Scott Mahlke | Ann Arbor, MI |
| 2005-2007 | University of Michigan M.S.E - Computer Science and Engineering <i>GPA: 8.5/9.0 (A=8.0, A+=9.0)</i> | Ann Arbor, MI |
| 2001-2005 | Sharif University of Technology B.S - Electrical Engineering <i>GPA: 17.2/20.0</i> | Tehran, Iran |

PUBLICATIONS

- | | |
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| PLDI'09 | Parallelizing Sequential Applications on Commodity Hardware using a Low-cost Software Transactional Memory Mojtaba Mehrara , Jeff Hao, Po-Chun Hsu, and Scott Mahlke To appear in the <i>ACM SIGPLAN 2009 Conference on Programming Language Design and Implementation (PLDI'09)</i> , June 2009 |
| PPoPP'09 | Transactional Memory with Strong Atomicity using Off-the-shelf Memory Protection Hardware Martin Abadi, Tim Harris, and Mojtaba Mehrara To appear in the <i>14th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP'09)</i> , Feb 2009 |
| TACO'08 | Exploiting Selective Placement for Low-cost Memory Protection Mojtaba Mehrara , and Todd Austin In the <i>ACM Transactions on Architecture and Code Optimization</i> , Vol. 5, Issue 3, Article 14, Nov 2008 |
| HPCA'08 | Uncovering Hidden Loop-Level Parallelism in Sequential Applications Hongtao Zhong, Mojtaba Mehrara , Steven Lieberman, and Scott Mahlke In Proc. of the <i>14th International Symposium on High-Performance Computer Architecture (HPCA'08)</i> , pp. 290 - 301, Feb 2008 |

HONORS and AWARDS

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| 2005 | Awarded the University of Michigan EECS Department graduate fellowship |
| 2005 | Awarded the University of Michigan EECS Department five-year guaranteed graduate funding |
| 2001 | Ranked 1st among 400,000 participants in Azad University entrance exam, Iran |
| 2001 | Ranked 9th among 350,000 participants in the nationwide university entrance exam, Iran |

GRADUATE COURSES

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| EECS 583 | Advanced Compilers |
| EECS 570 | Parallel Computer Architecture |
| EECS 470 | Computer Architecture |
| EECS 427 | VLSI Design I |
| EECS 573 | Microarchitecture |
| EECS 523 | Digital Integrated Circuit Technology |
| EECS 586 | Design and Analysis of Algorithms |
| EECS 492 | Artificial Intelligence |

SERVICE

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|------------|--|
| Web Chair: | <ul style="list-style-type: none">• 2008 Workshop on Parallel Execution of Sequential Programs on Multi-core Architectures (PESPMA'08), Beijing, China, June 2008 |
| Reviewer: | <ul style="list-style-type: none">• International Symposium on Computer Architecture (ISCA'07, ISCA'08)• International Conference on Microarchitecture (MICRO'07)• International Symposium on High-Performance Computer Architecture (HPCA'08)• International Symposium on Code Generation and Optimization (CGO'08)• ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP'09)• Design Automation and Test in Europe (DATE'07) |