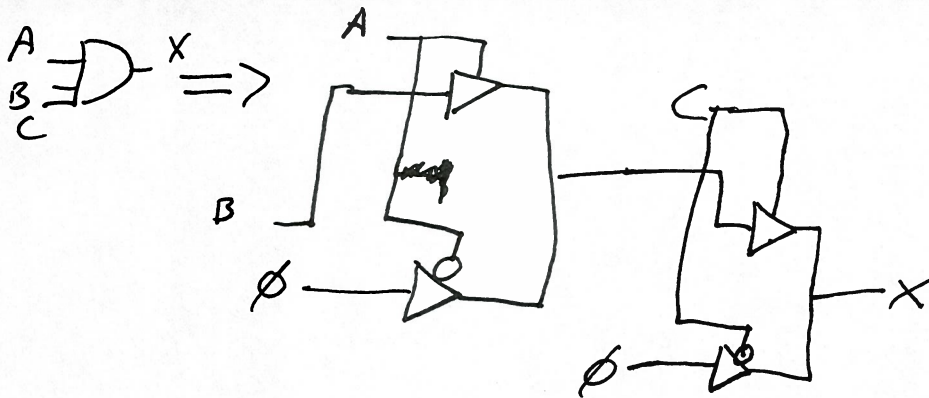
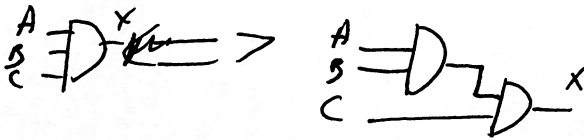


# Quiz 3 – Spring 2011 – EECS 270

Name: KEY \_\_\_\_\_ unname: \_\_\_\_\_

This quiz is graded out of 100 points. Please remember you can drop your lowest quiz score. You will have 20 minutes for the quiz. It is closed book and closed notes. Show your work and circle your answer!

1. Using only inverters and tri-state devices, build a 3-input AND gate. [40]



2. Design a Moore-type state transition diagram which takes one input "X" and generates one output "Y". Y should be a "1" if at least 2 of the last 3 values of X were a "1". Your answer must have 10 or fewer states. [60]

