# CSE Terminal Masters Plan of Study in VLSI

**Name:**

**UMID:**

**Advisor (signature required):**

**Date:**

## MSE Degree or MS Degree (circle one)

(What is your undergrad degree field? (engineering or non-engineering))

## MSE Degree Term:

### Credit Hours

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<th>Course</th>
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### Technical Electives: at least 15 credit hours

- MSE 627 and 627 (Both)
- 427 and 627 (Both)
- At least 24 credit hours
- At least 15 credit hours

### VLSI Kernel Requirements

- (Two of 400 level: 482; 483; 484; 485; 487; 489; 490 or 571 or 582 or 583 or 584 or 587 or 588 or 589 or 590 or 591 (Software) or 592 (Hardware)
- CSE Courses 500 level or above at least 15 credit hours
- 543 or 545 or 592
- 470 or 473 or 478 or 527 or 570 or 572 or 573 or 577 or 578 or 579 or 581 or 583
- 574 or 575 or 586

### Notes:

1. A maximum of six credit hours of individual study, research, and seminars
2. You must meet all Rackham and Program requirements (see brochures for details)
3. It is expected that most entering students will have already completed courses equivalent to (492 and 478) and 492 and 478
4. Seminar, directed study credits (except 3 hrs. of EECS 599) do not count toward the 500 level course requirement
5. It is the student’s responsibility to see that all requirements are met.
6. You must choose 2 of the 4 areas in addition to the VLSI Kernel
7. one of the 500 level must be from the approved list/see brochu
8. If you already have a masters degree from another institution that has been deemed relevant by CSE you are not eligible for a masters degree from this program.

### Total Hours:

**Total Hours:**

(9 credits in total)

### GPA

**GPA:**

**CTP:**

08/16